

FIG. 1A

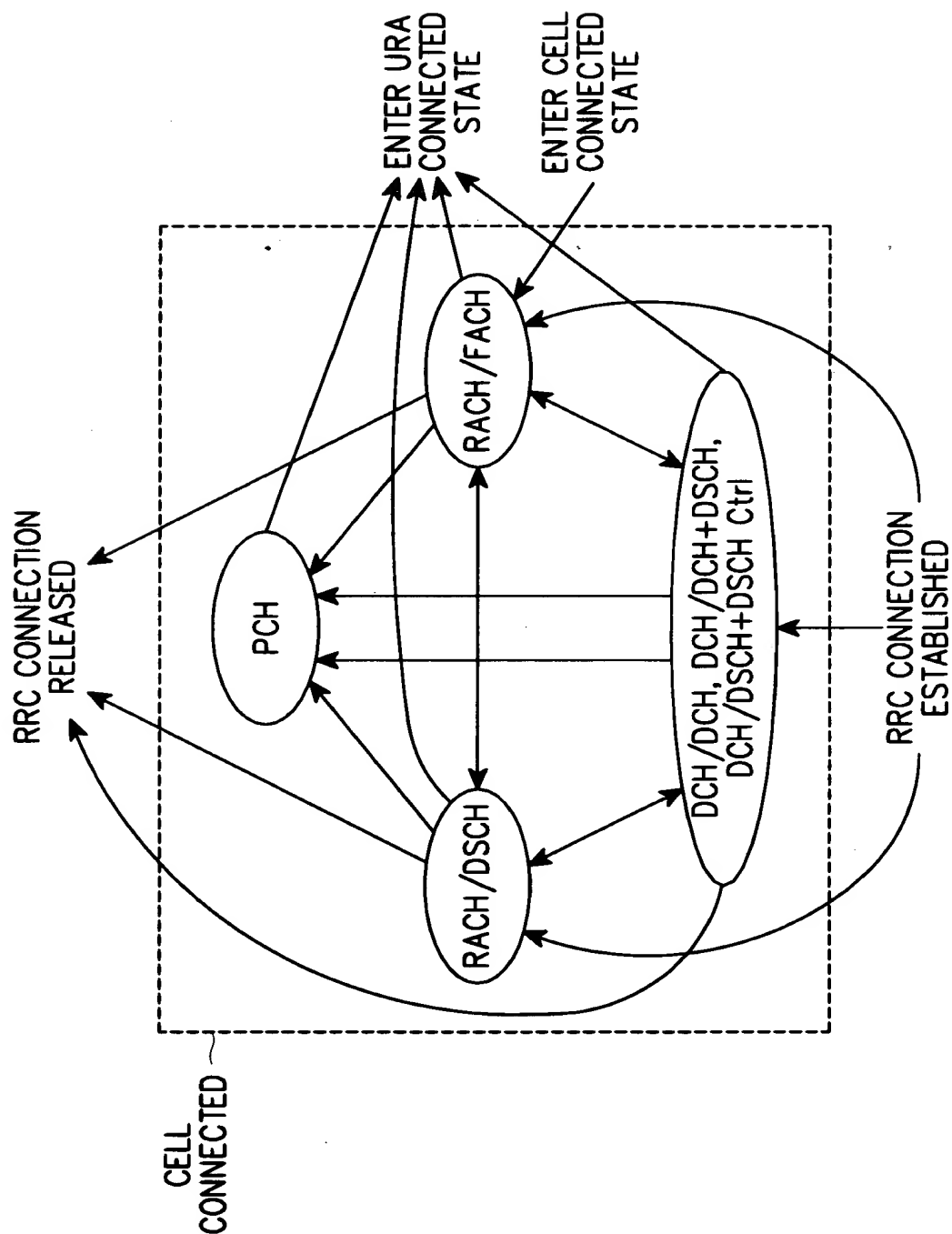


FIG. 1B

DCH/DCH, DCH/DCH+DSCH, DCH/DSCH+DSCH Ctrl

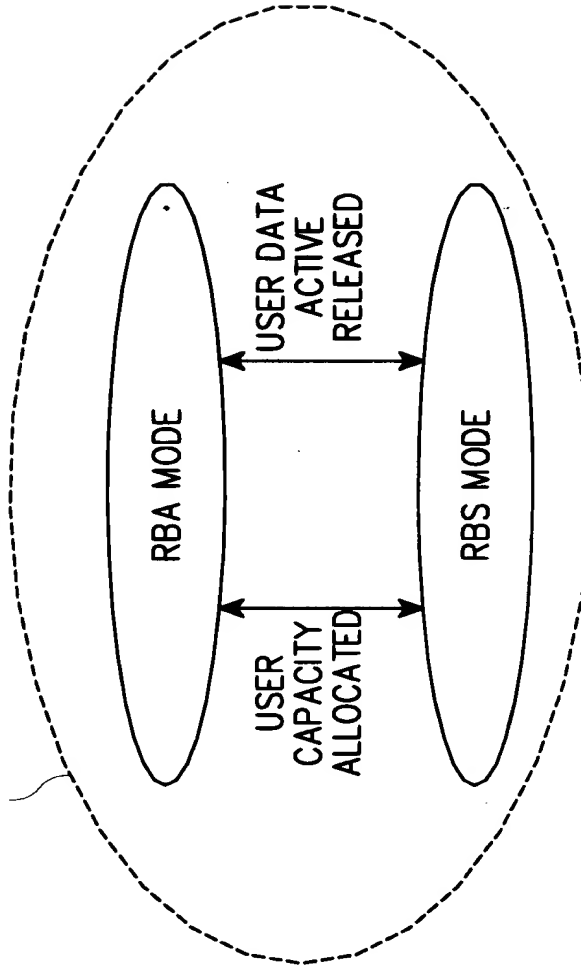


FIG. 2A

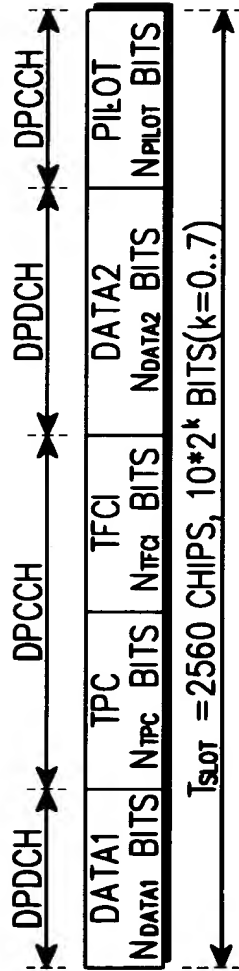


FIG. 2B

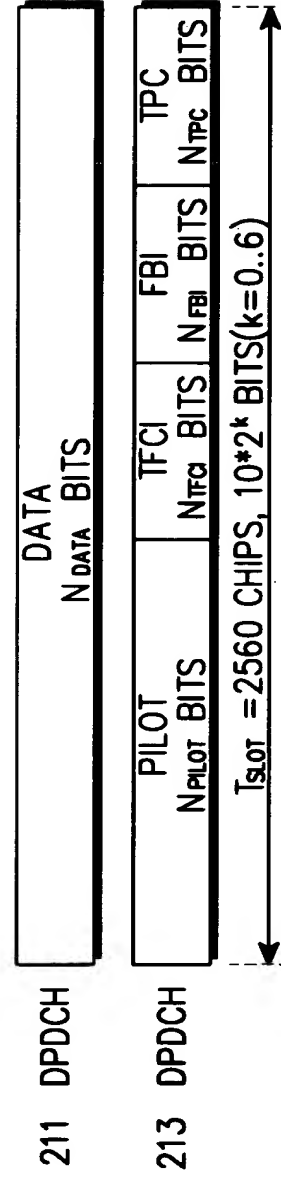


FIG. 3

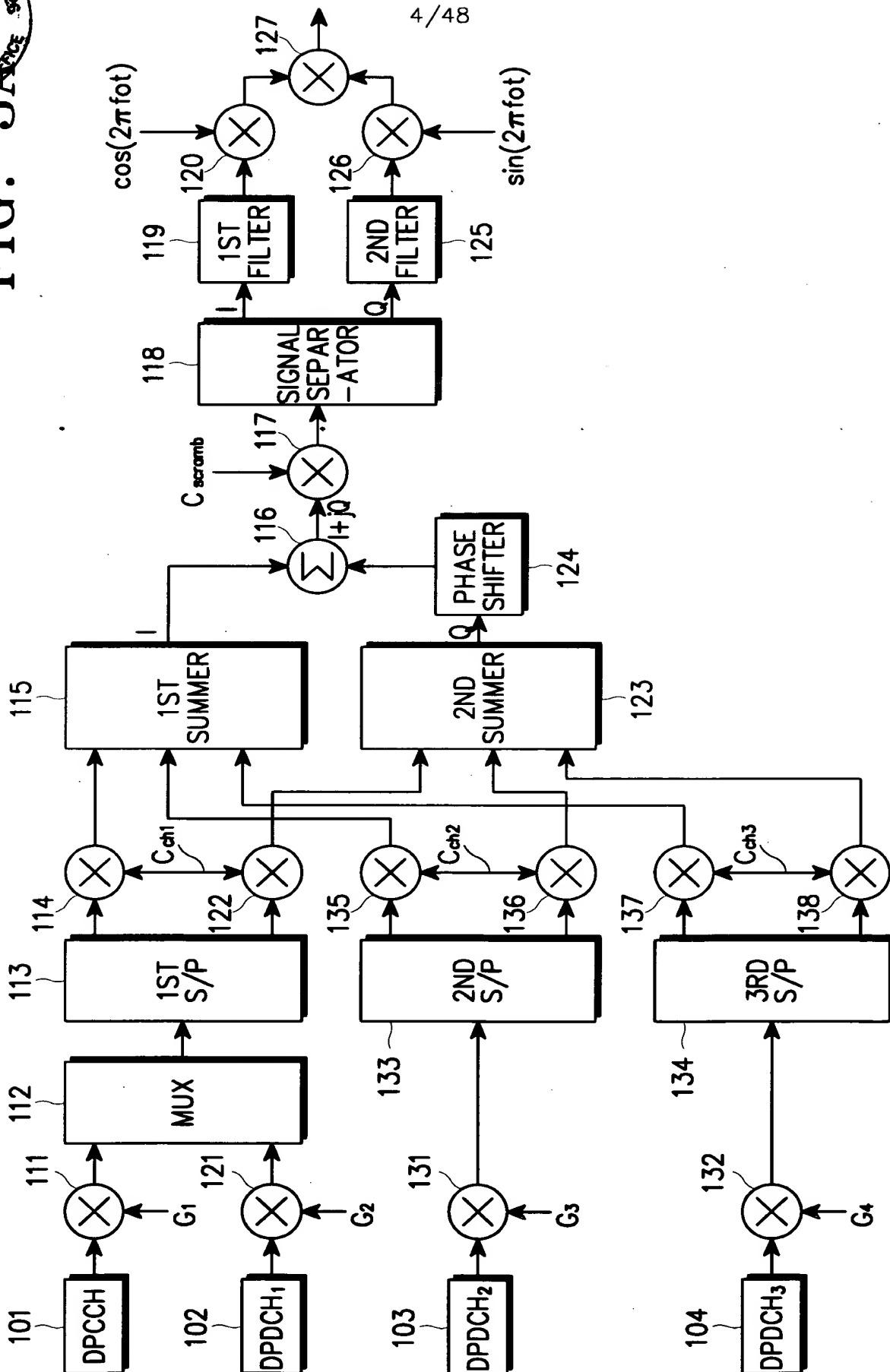


FIG. 3B

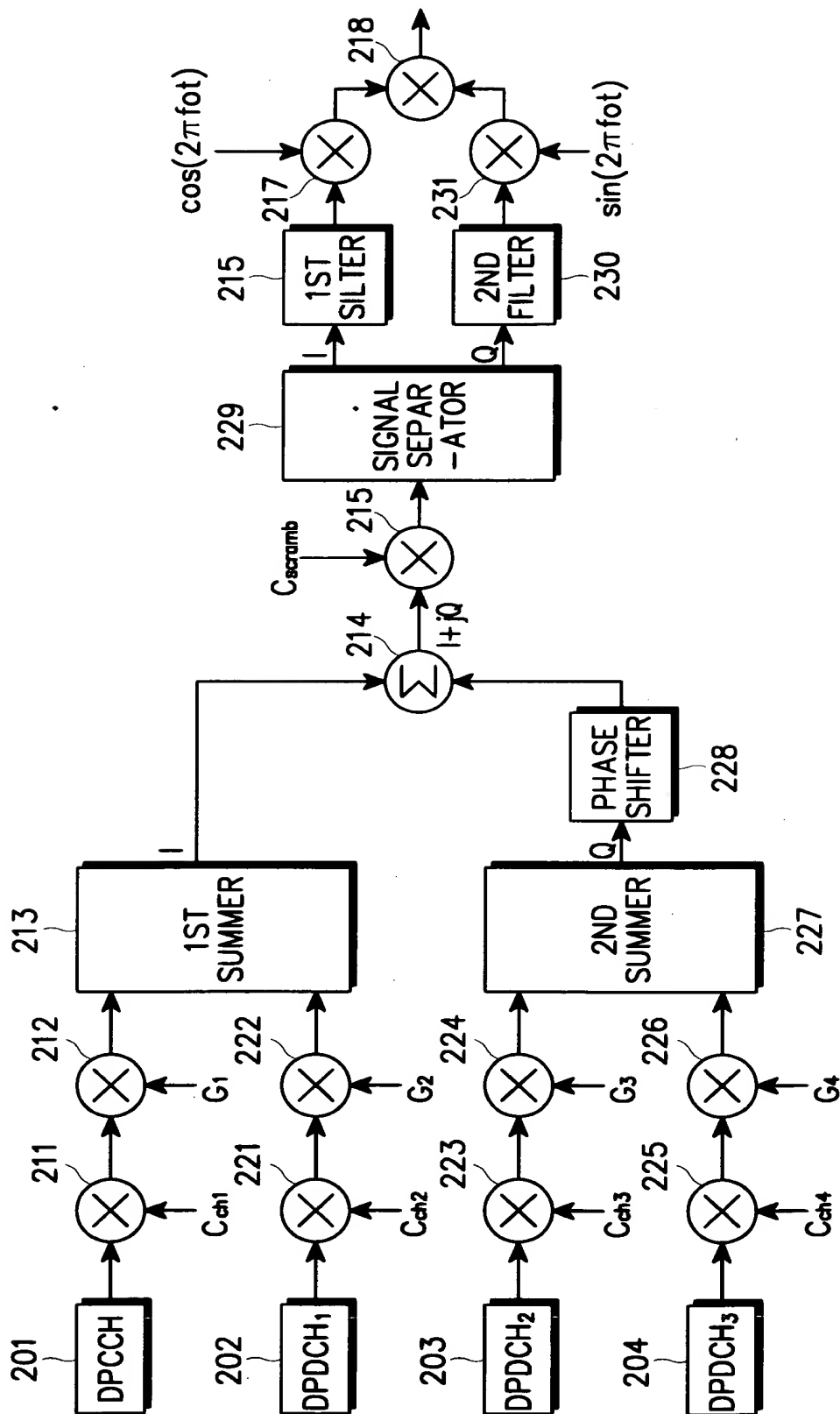


FIG. 4A

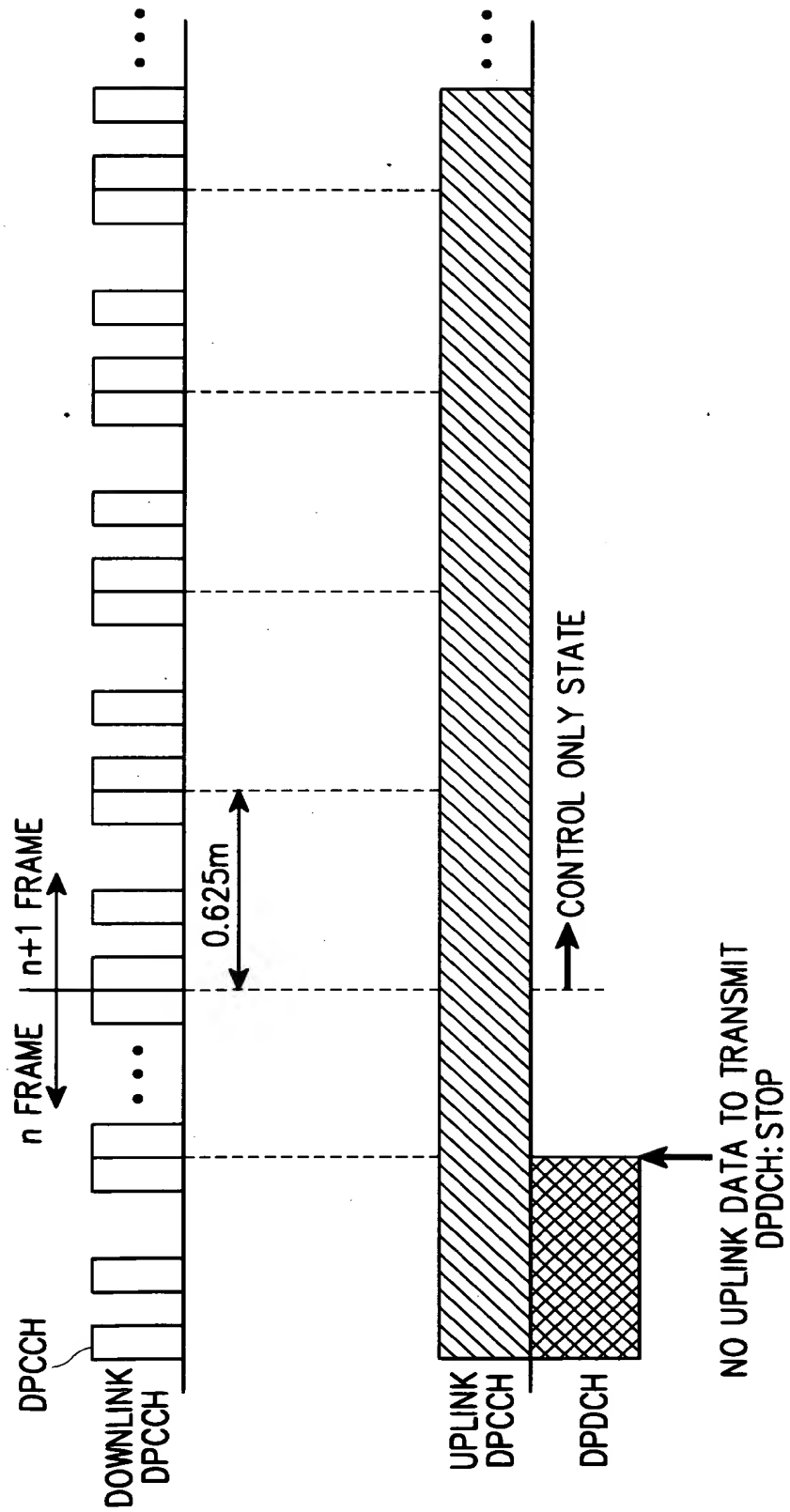
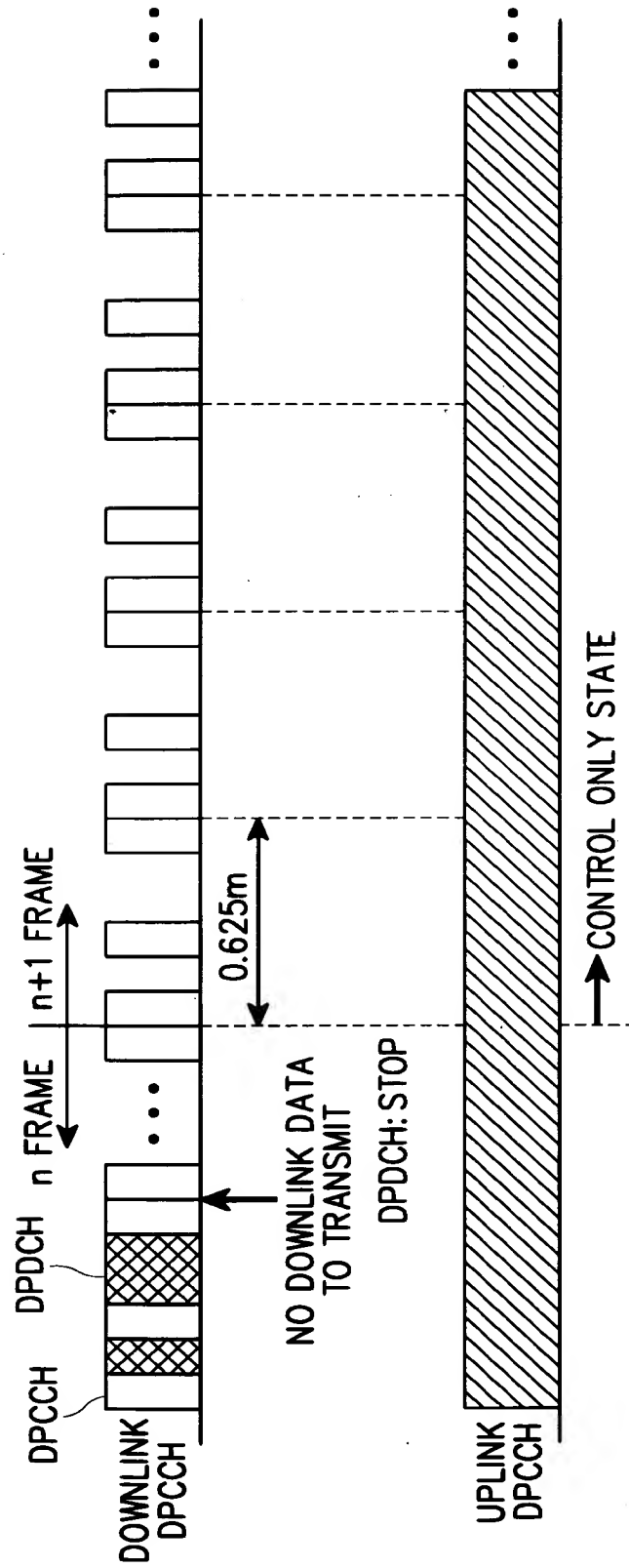


FIG. 4B



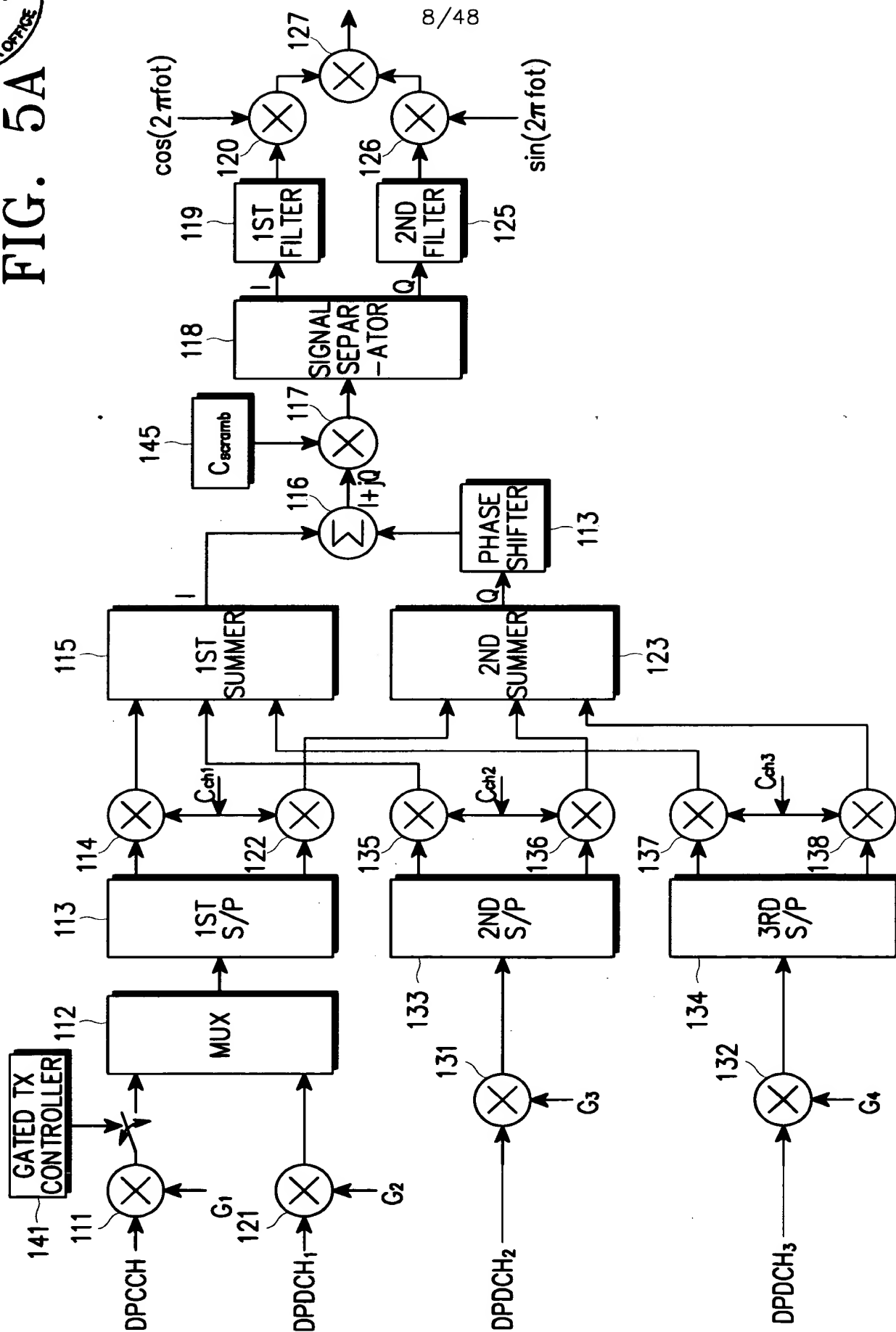


FIG. 5B

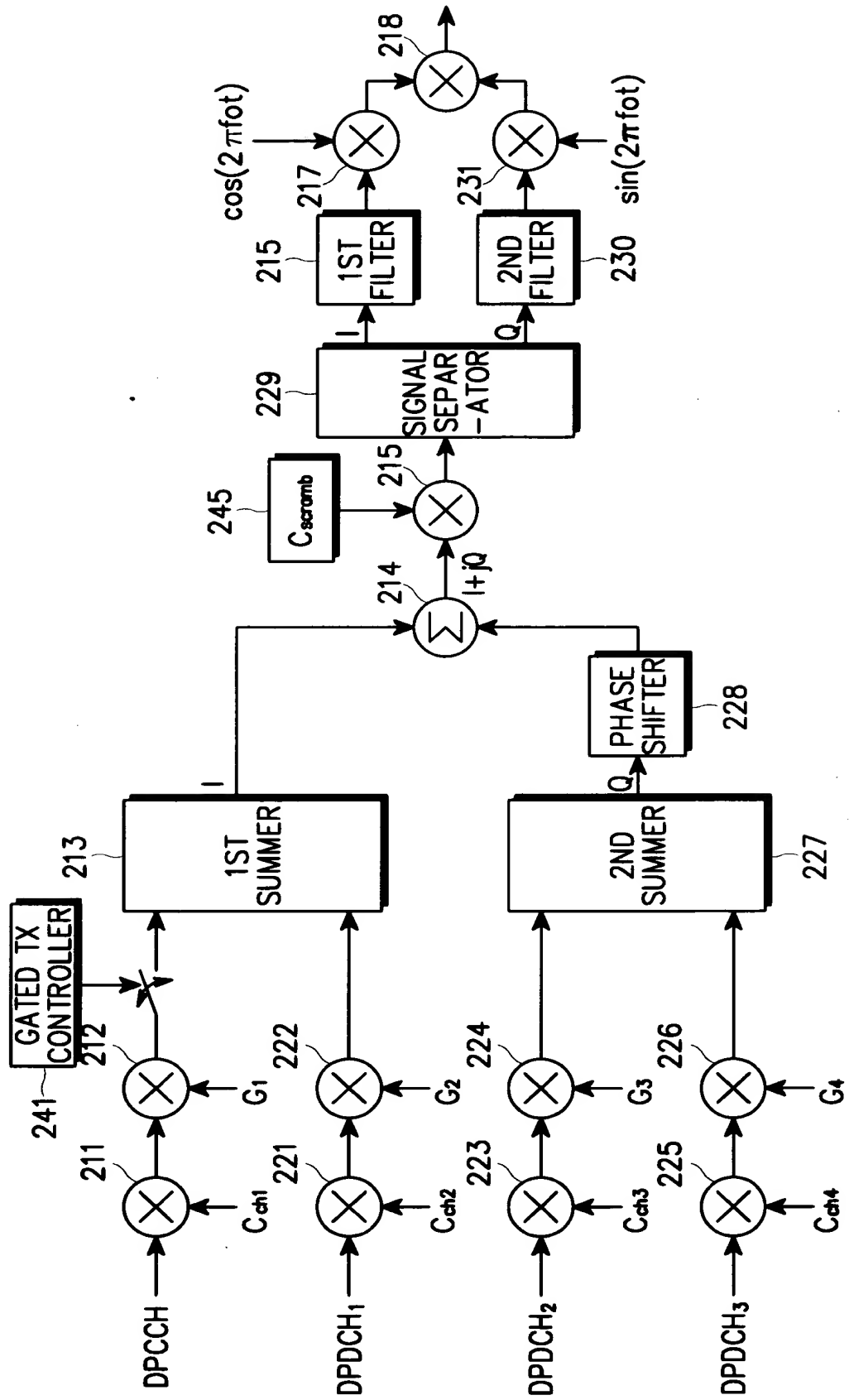


FIG. 5C

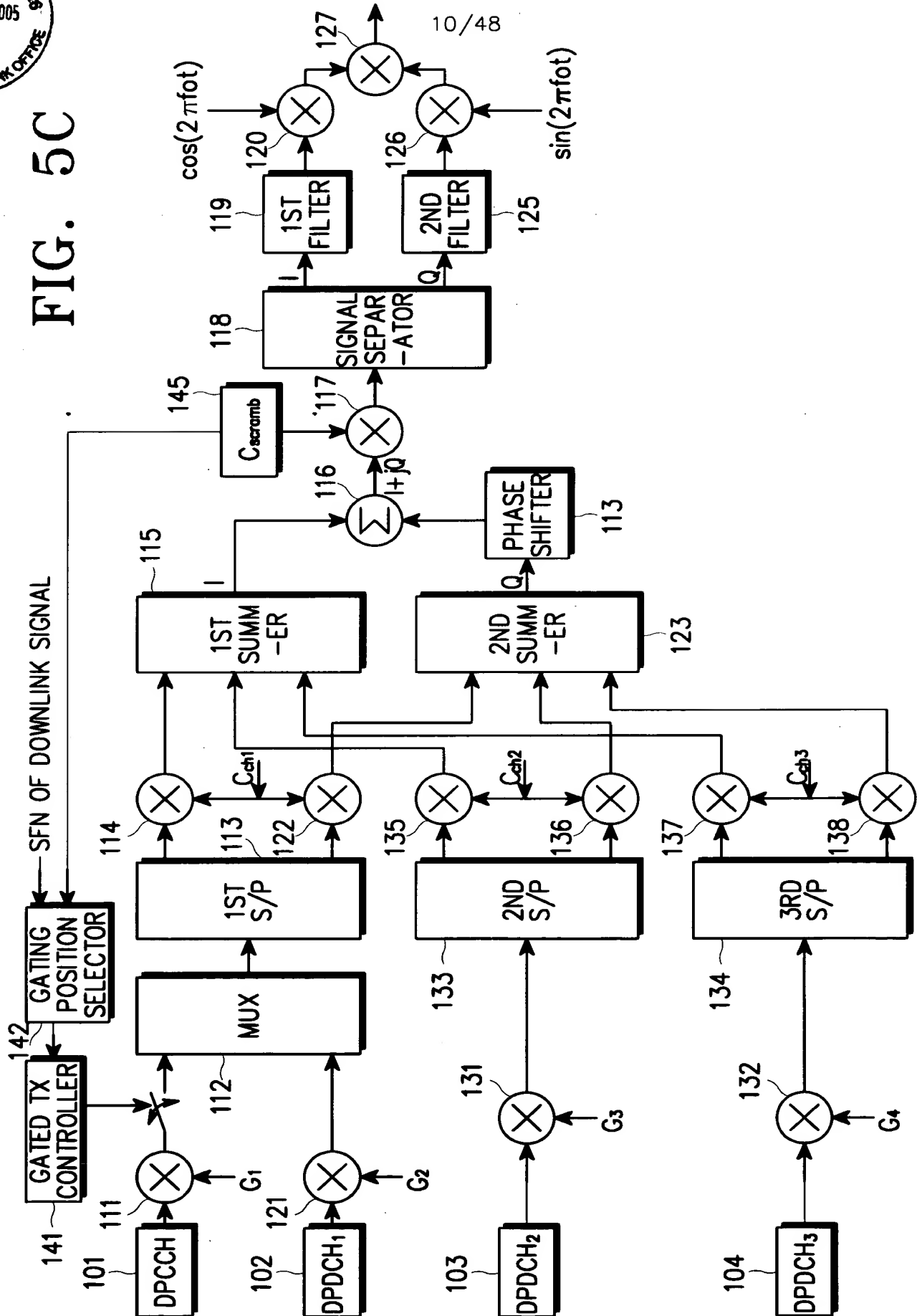


FIG. 5D

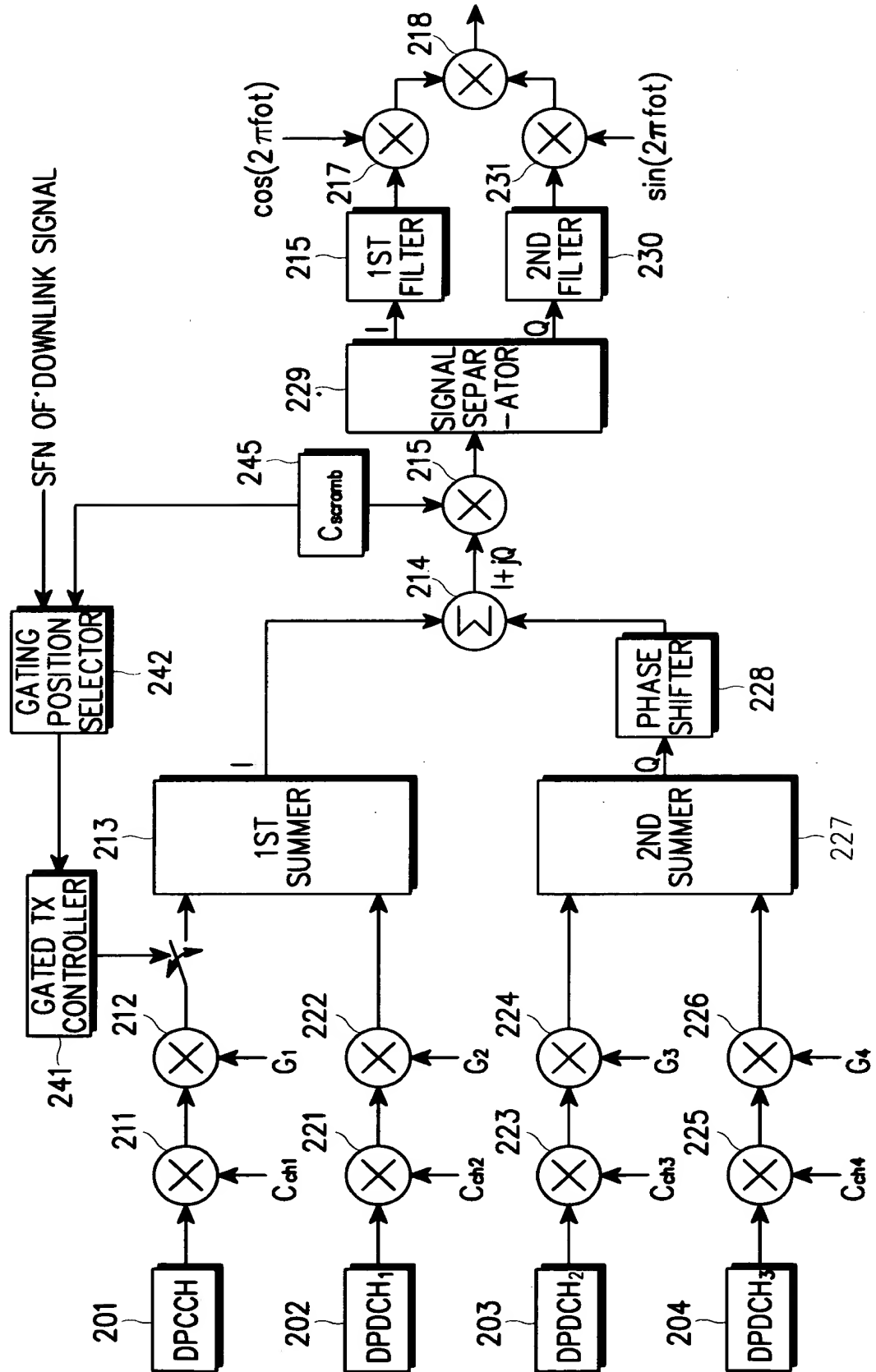
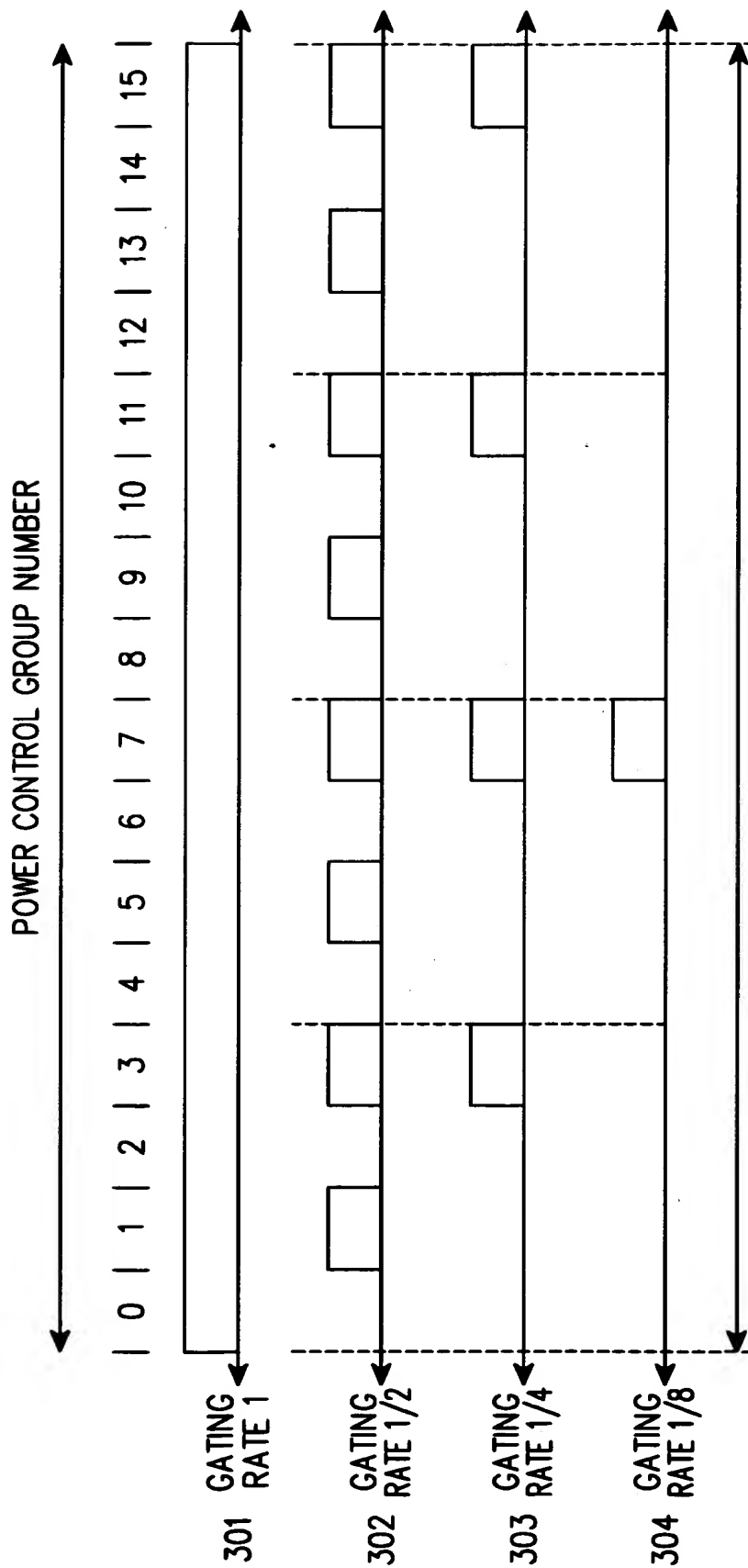


FIG. 6A





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FIG. 6B

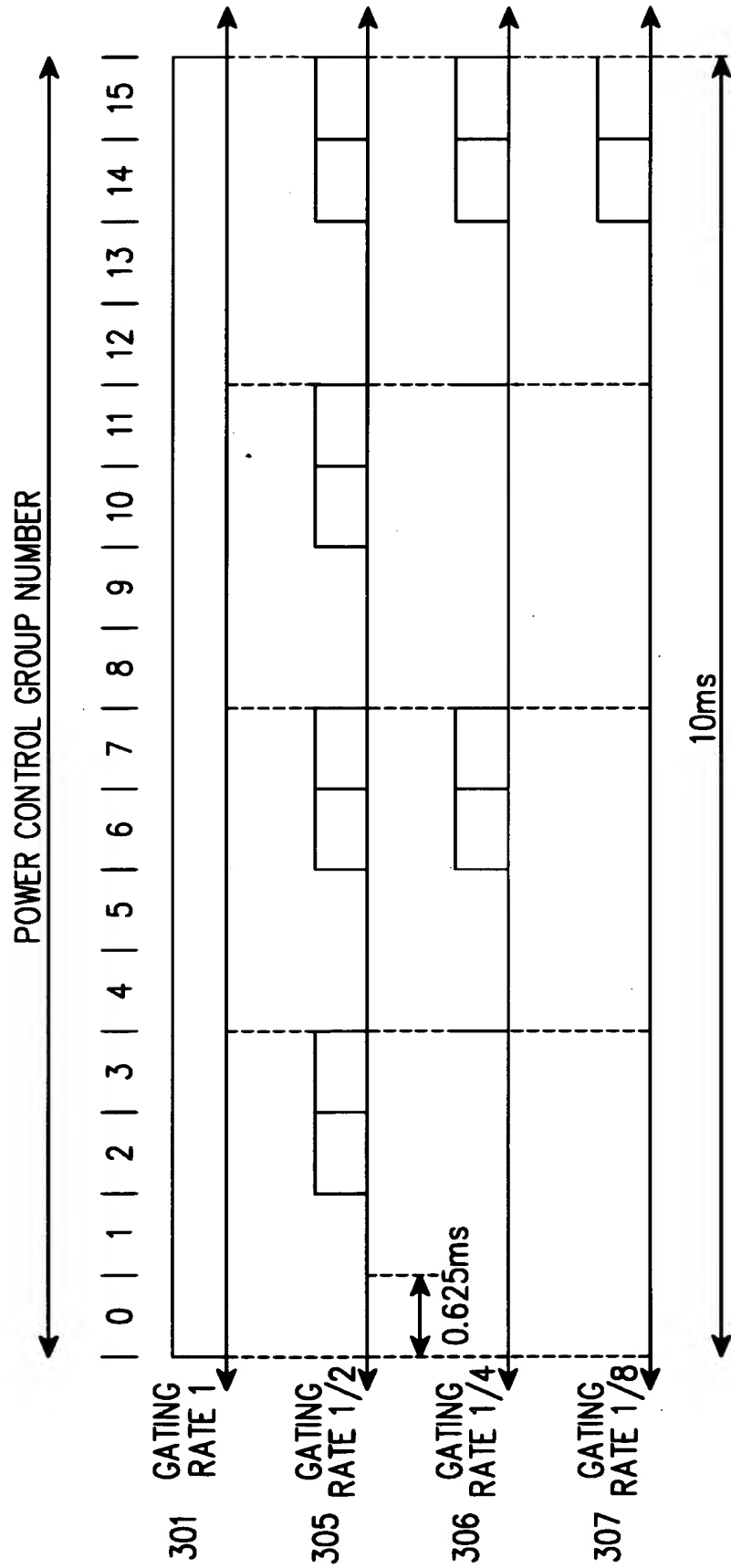


FIG. 7A

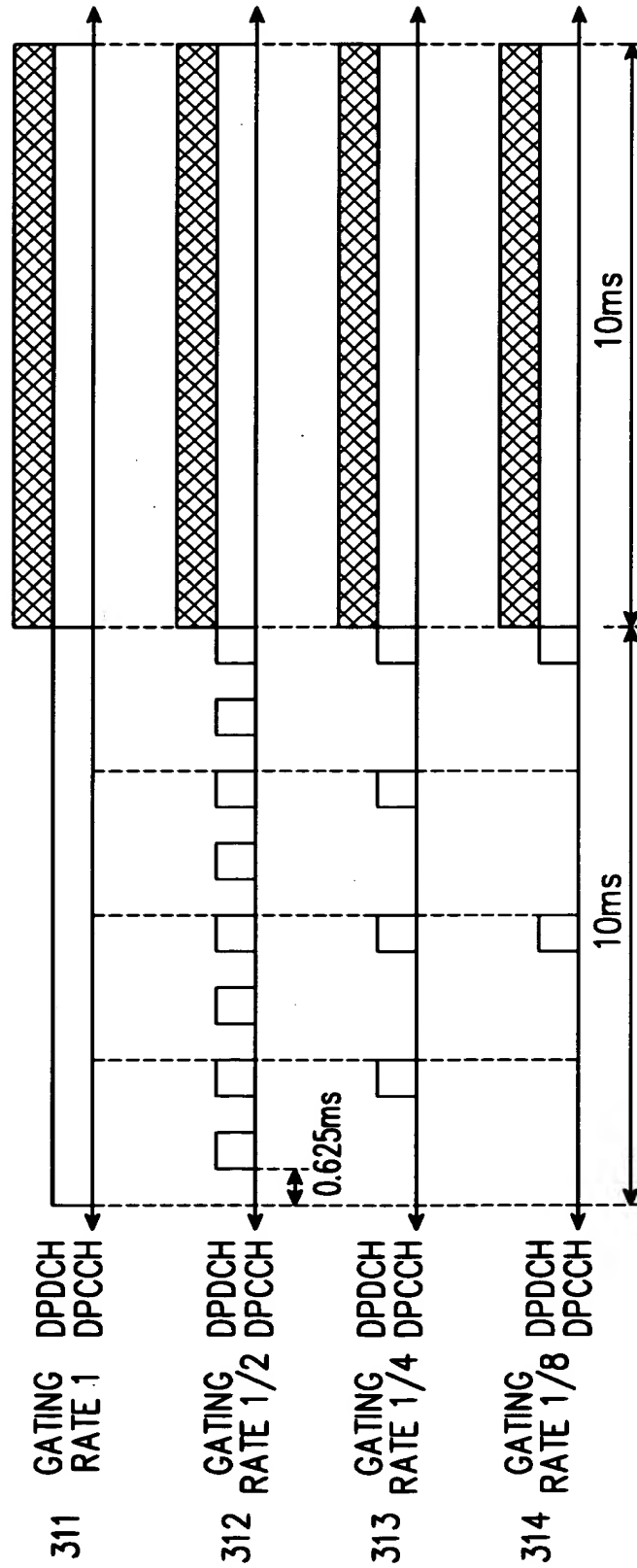


FIG. 7B

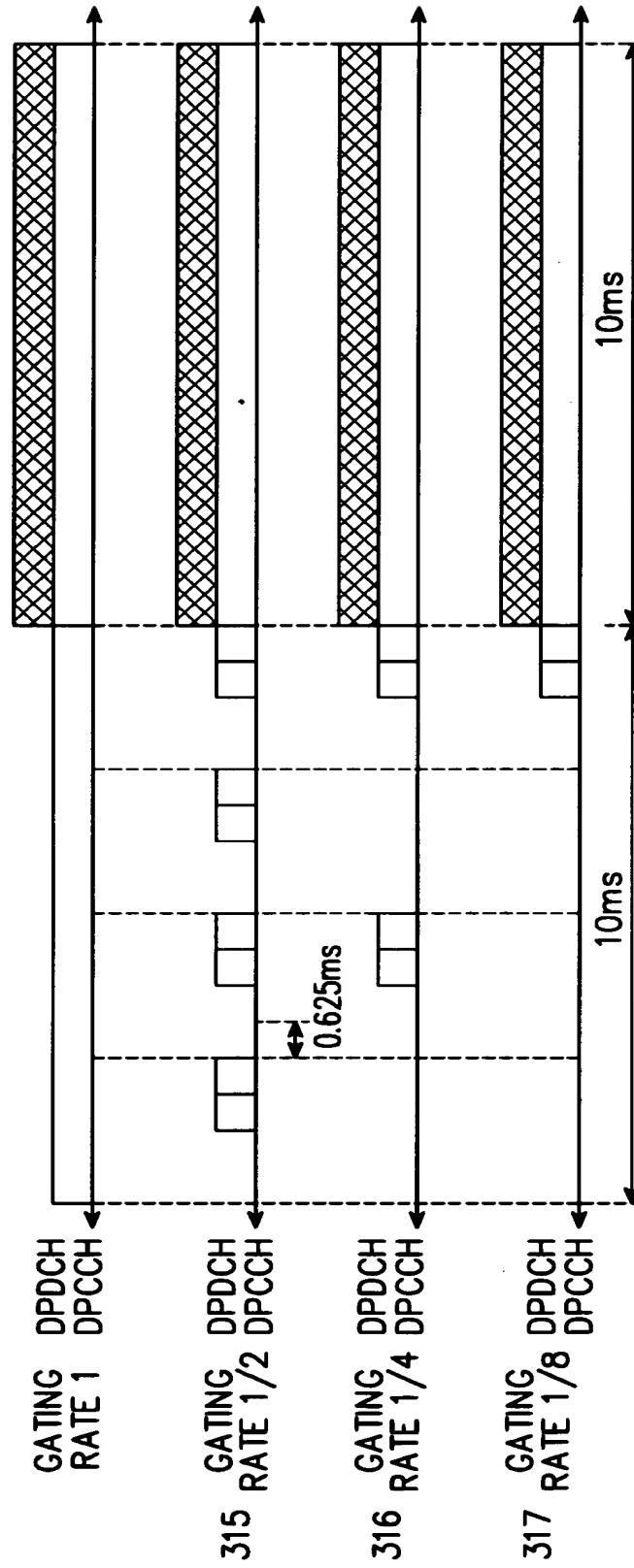
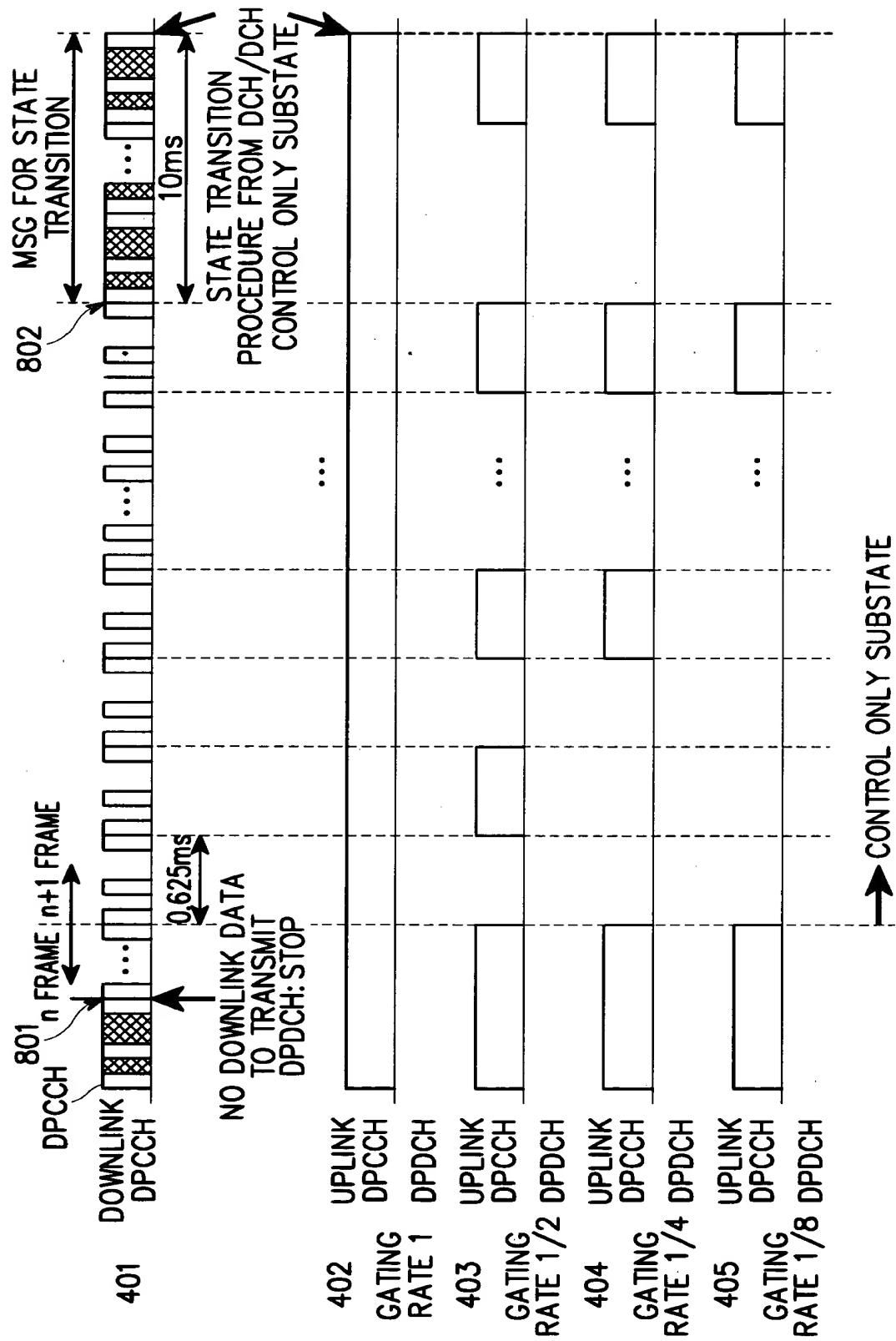


FIG. 8A



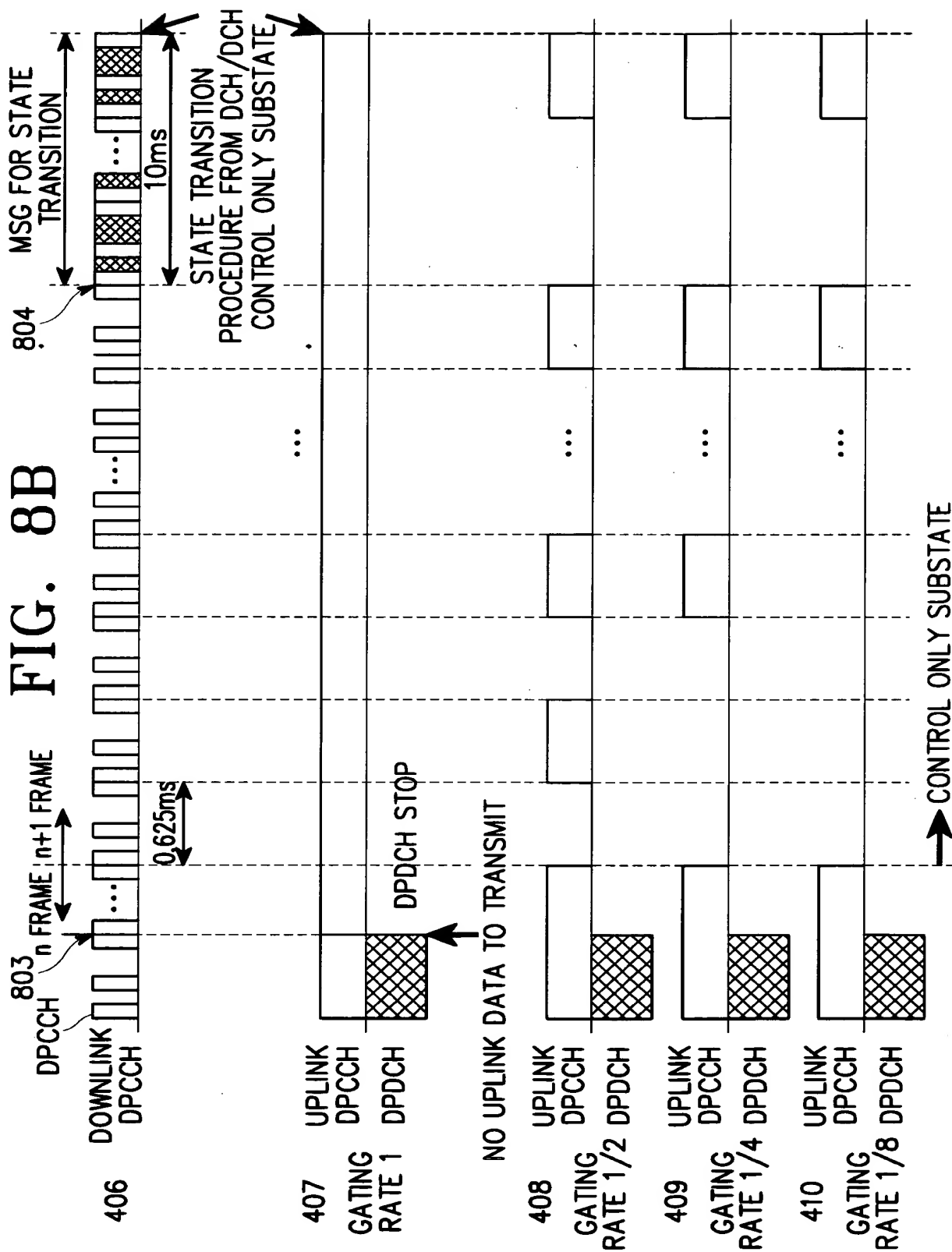


FIG. 8C

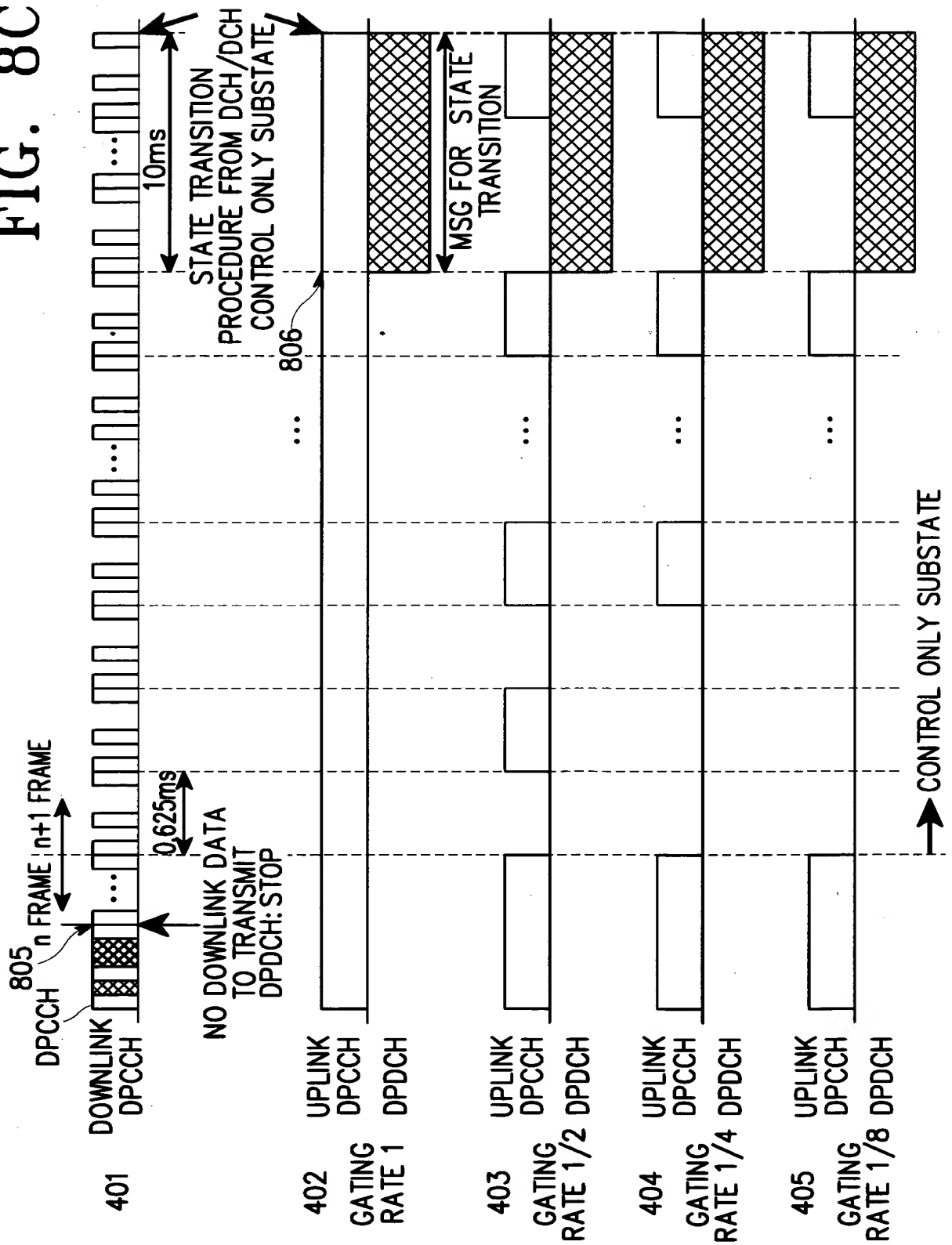


FIG. 8D

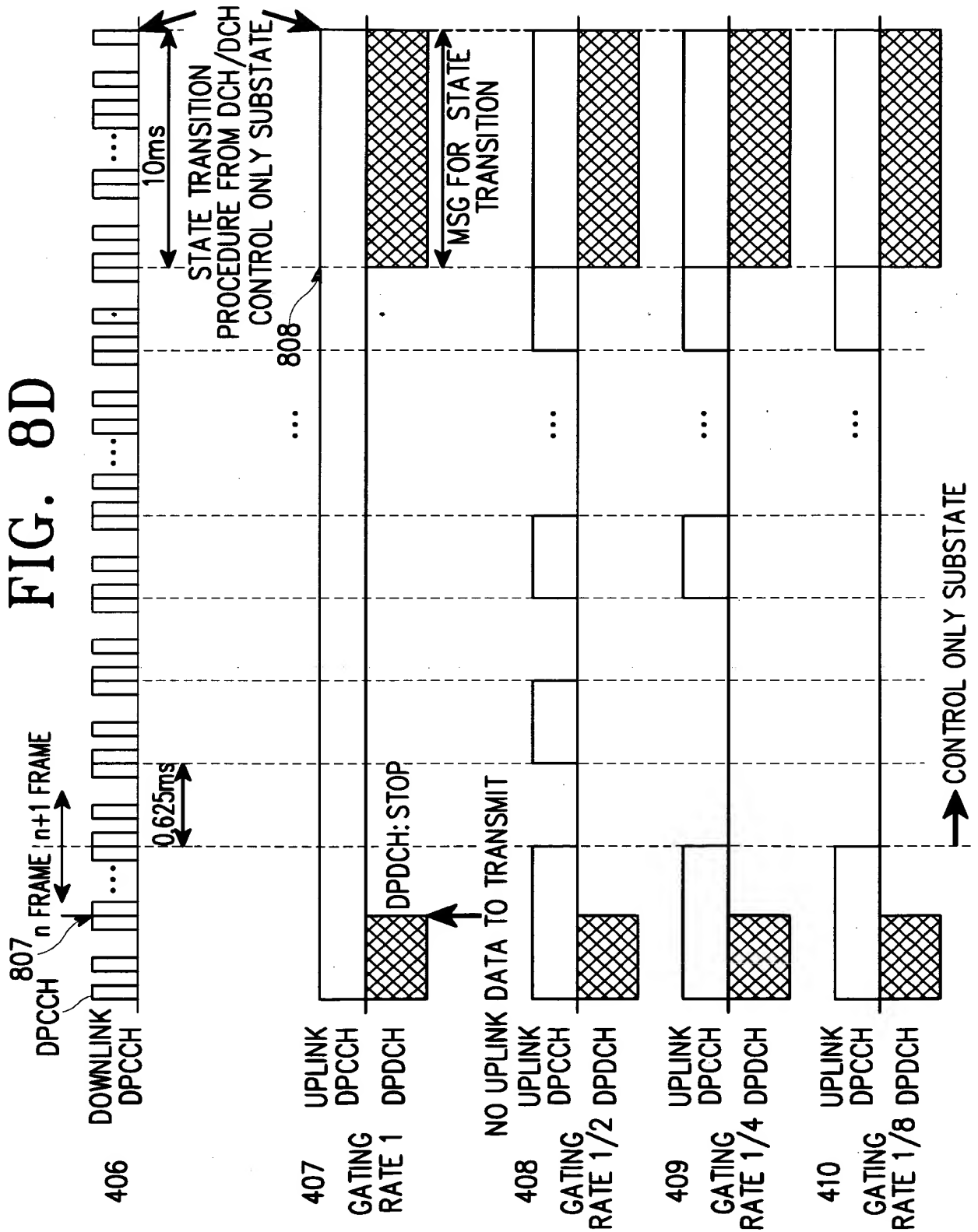


FIG. 9A

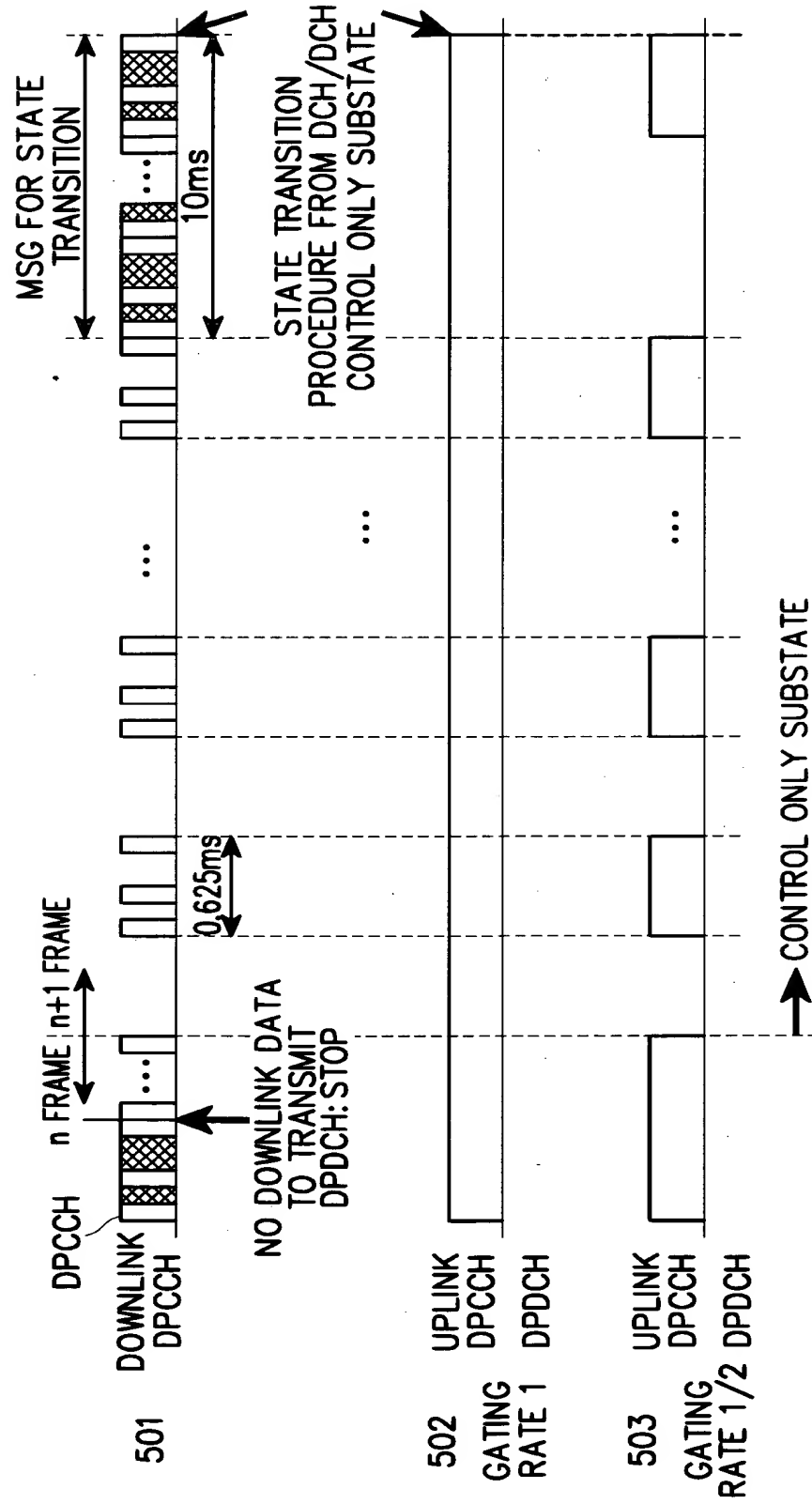


FIG. 9B

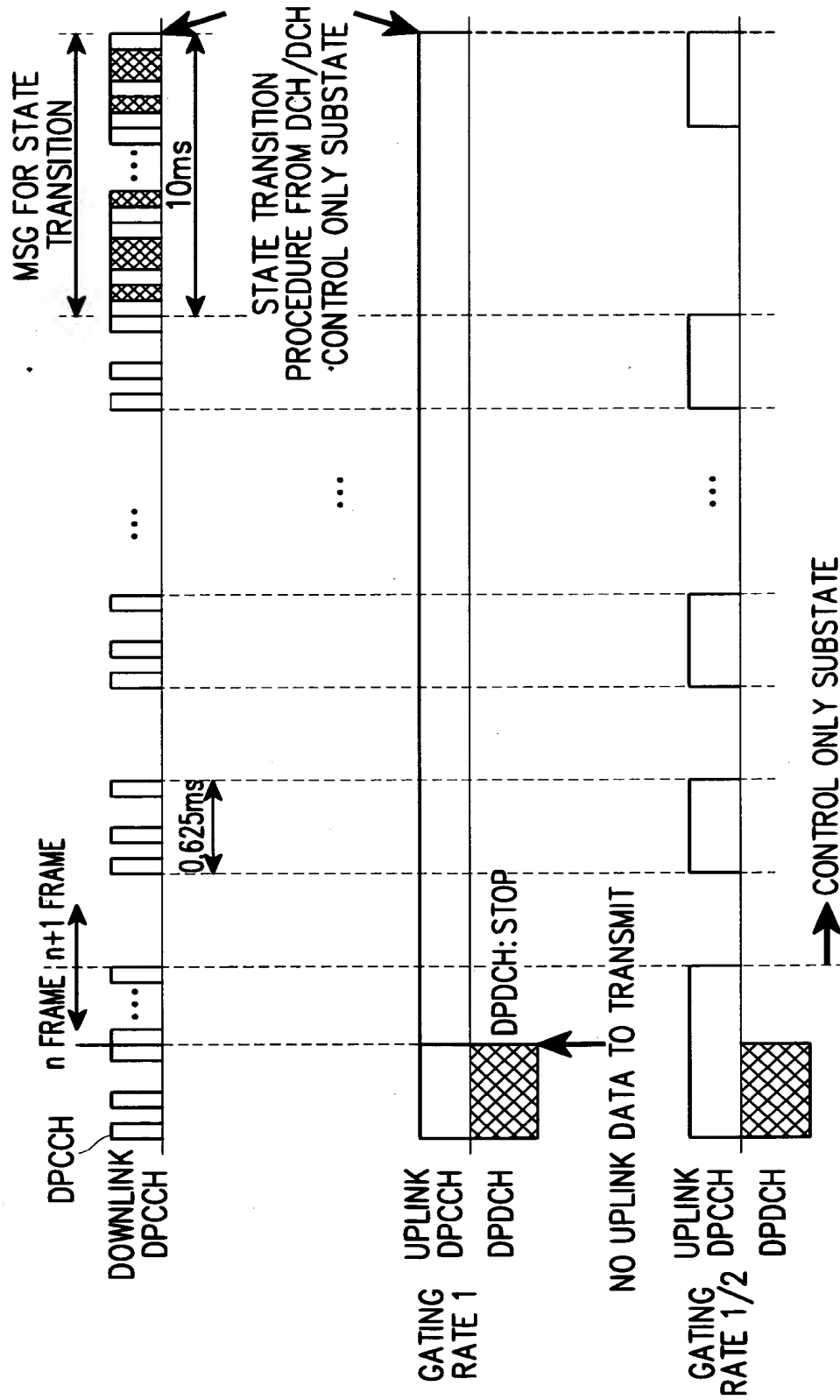


FIG. 10A

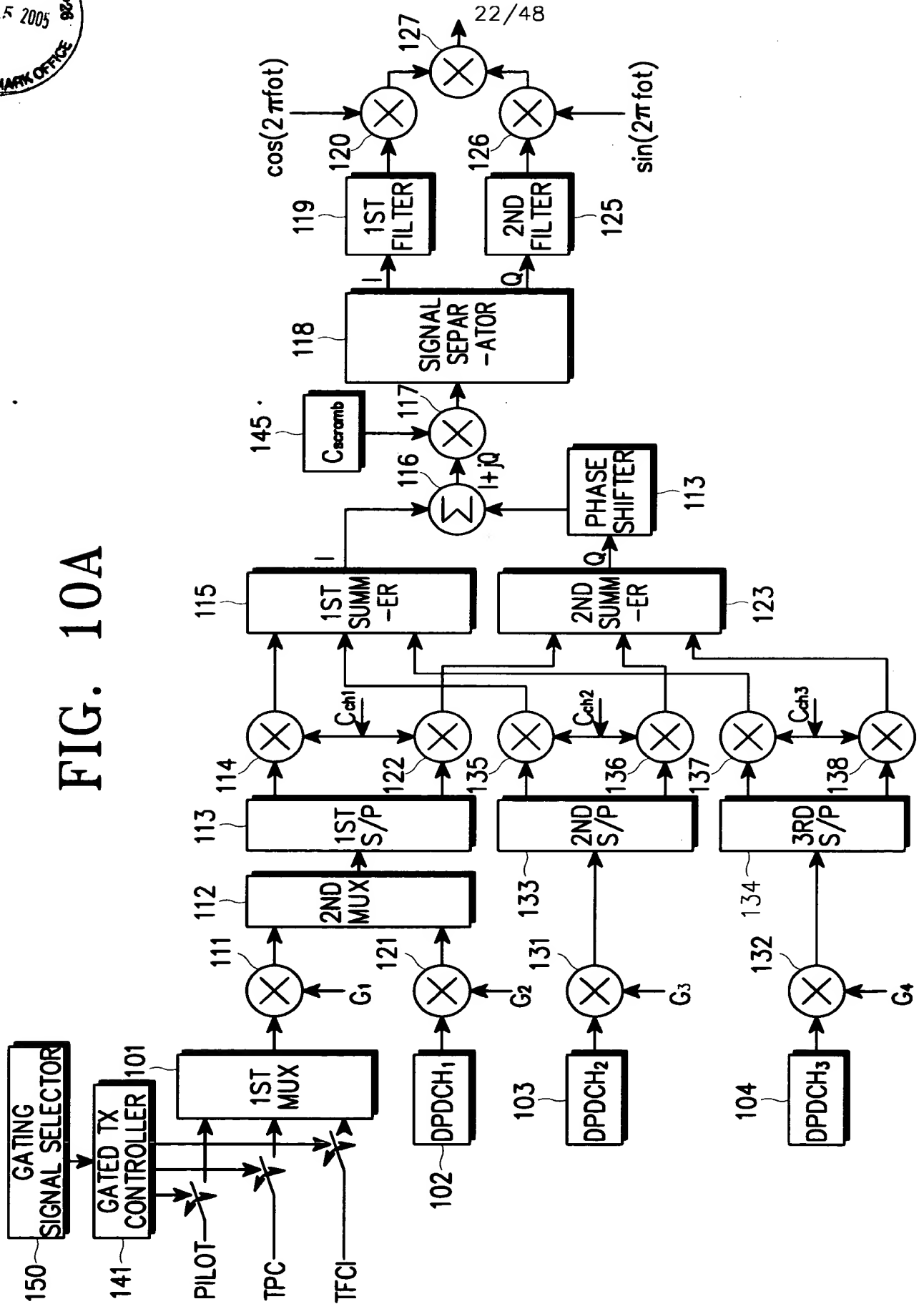
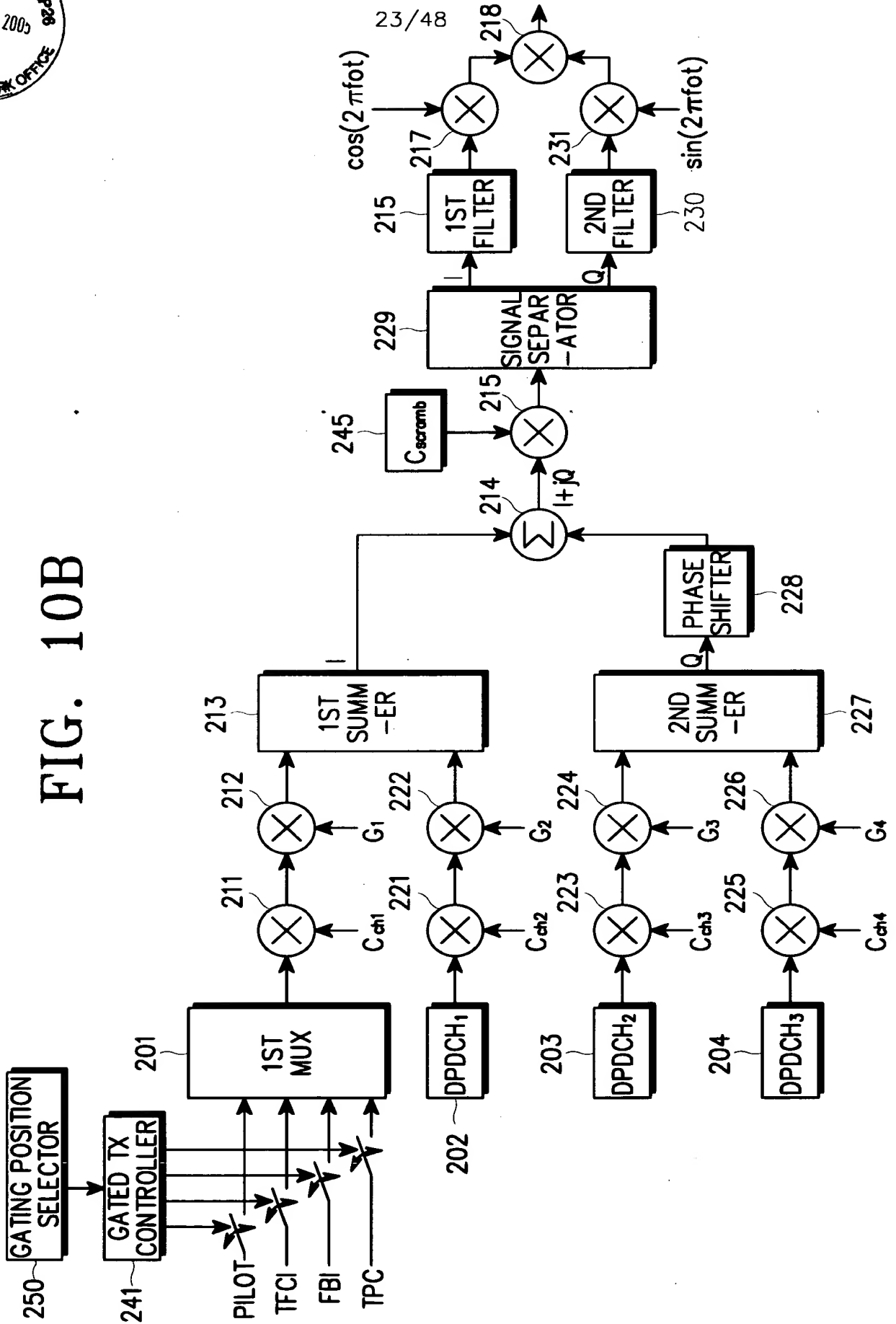


FIG. 10B



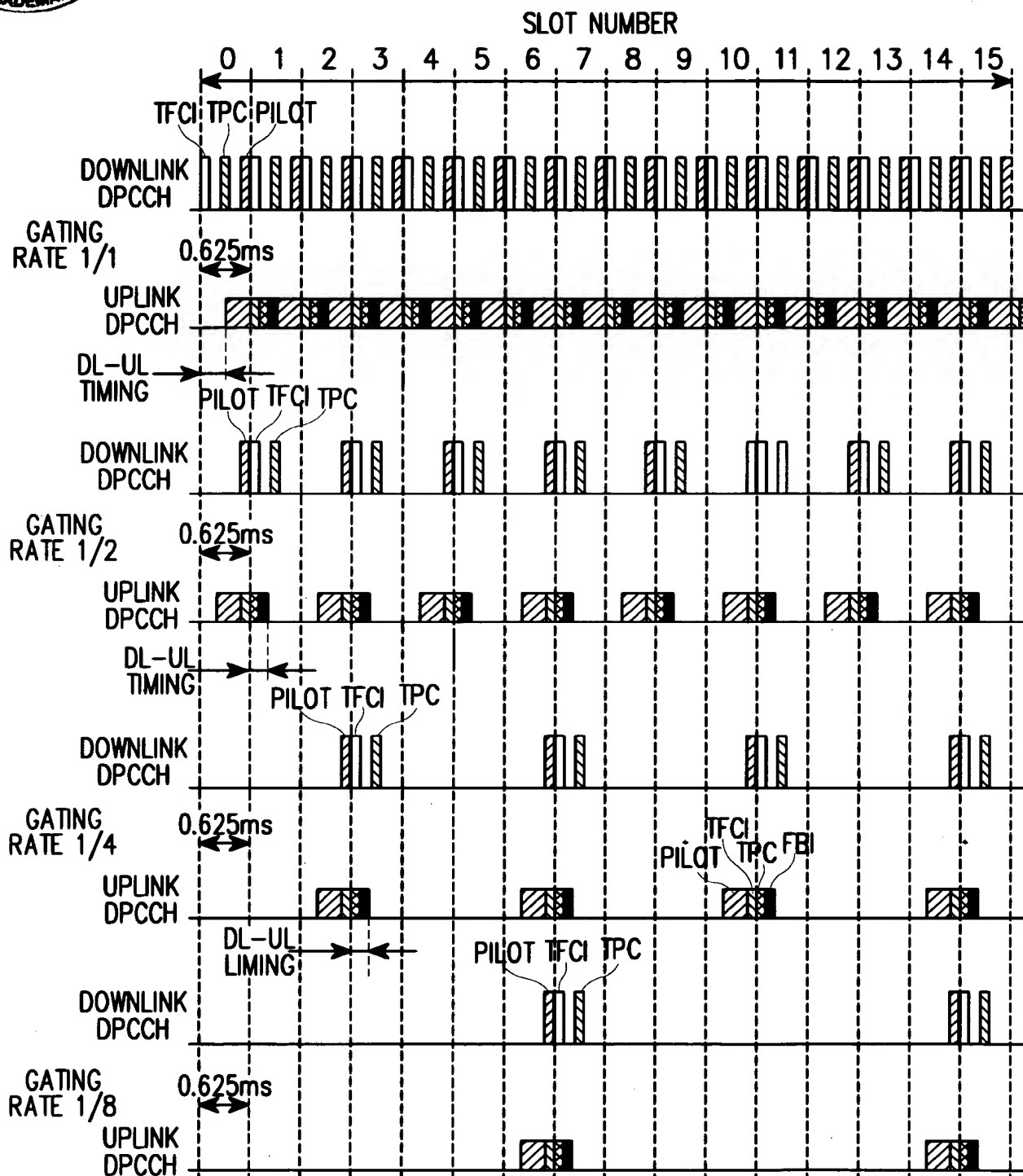


FIG. 11A

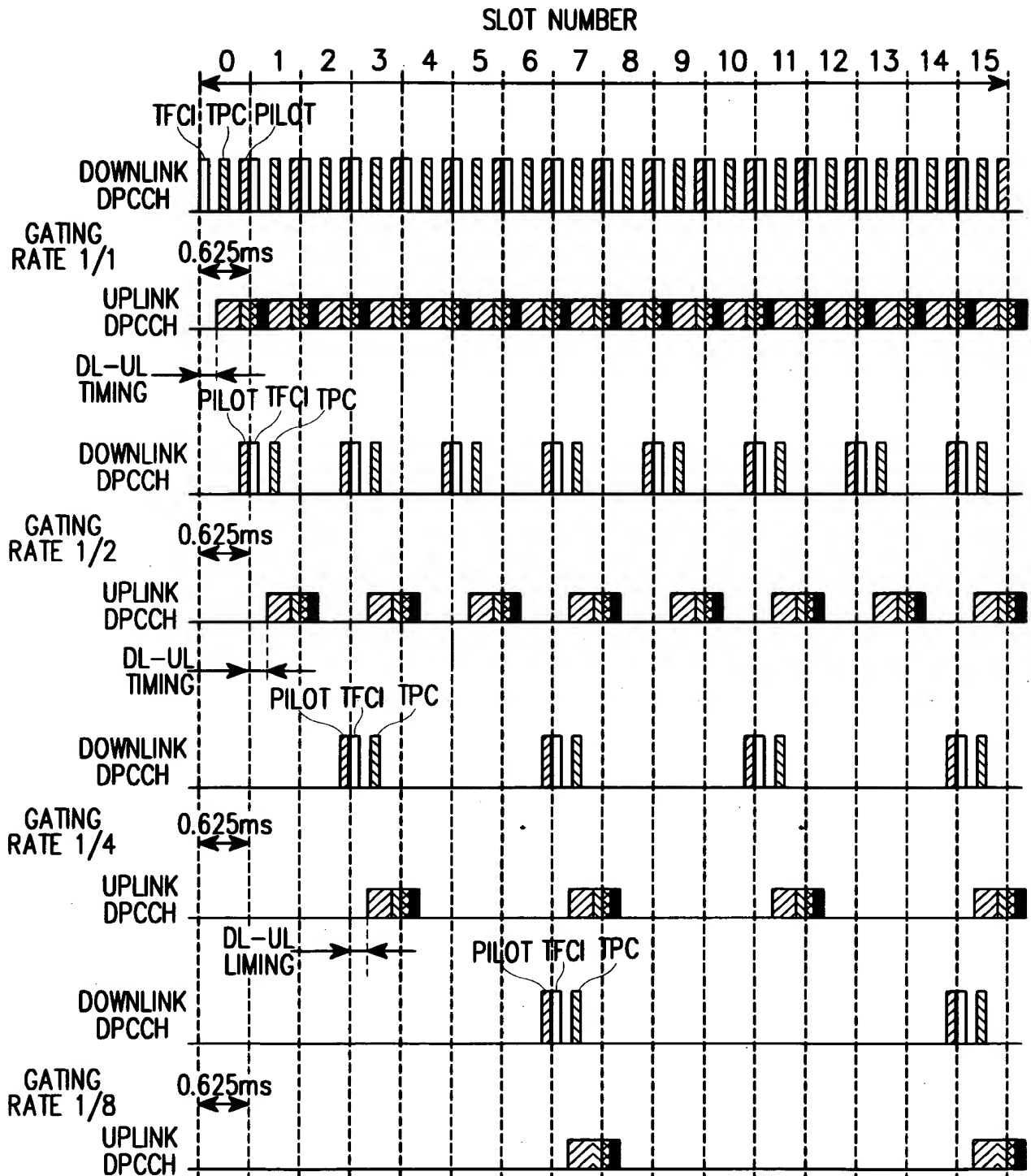


FIG. 11B

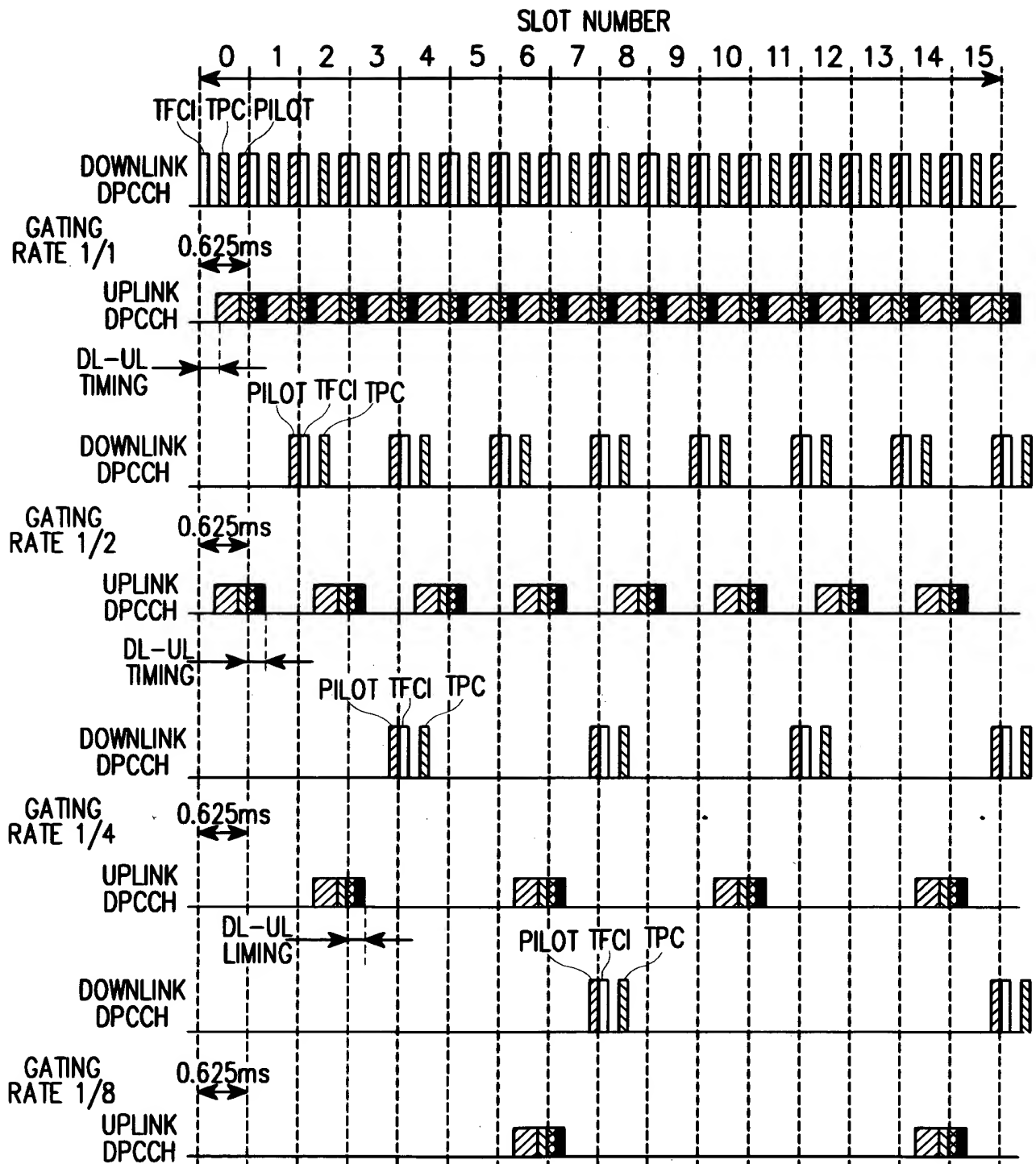


FIG. 11C

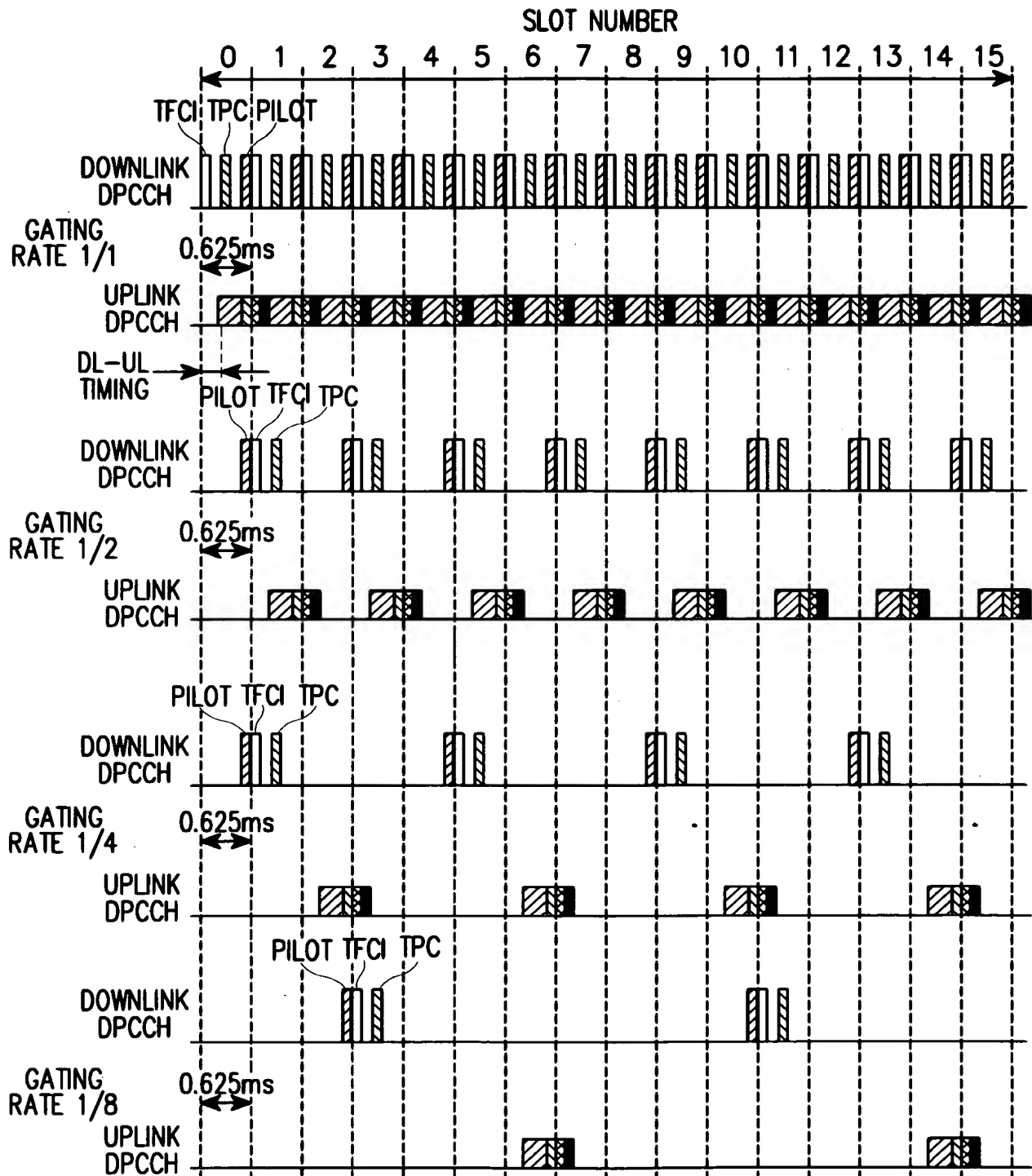


FIG. 11D

FIG. 12A

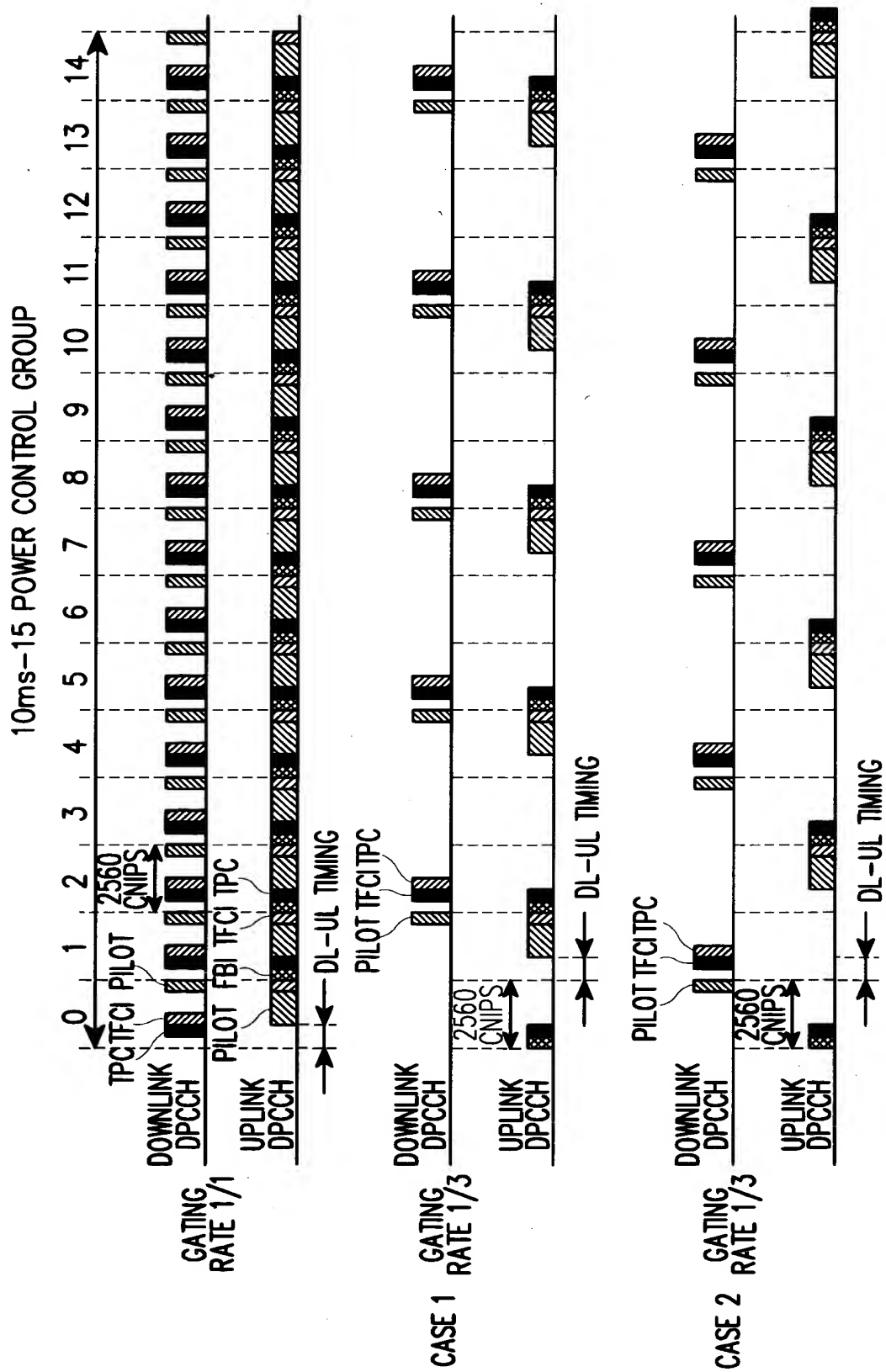


FIG. 12B

10ms-15POWER CONTROL GROUP

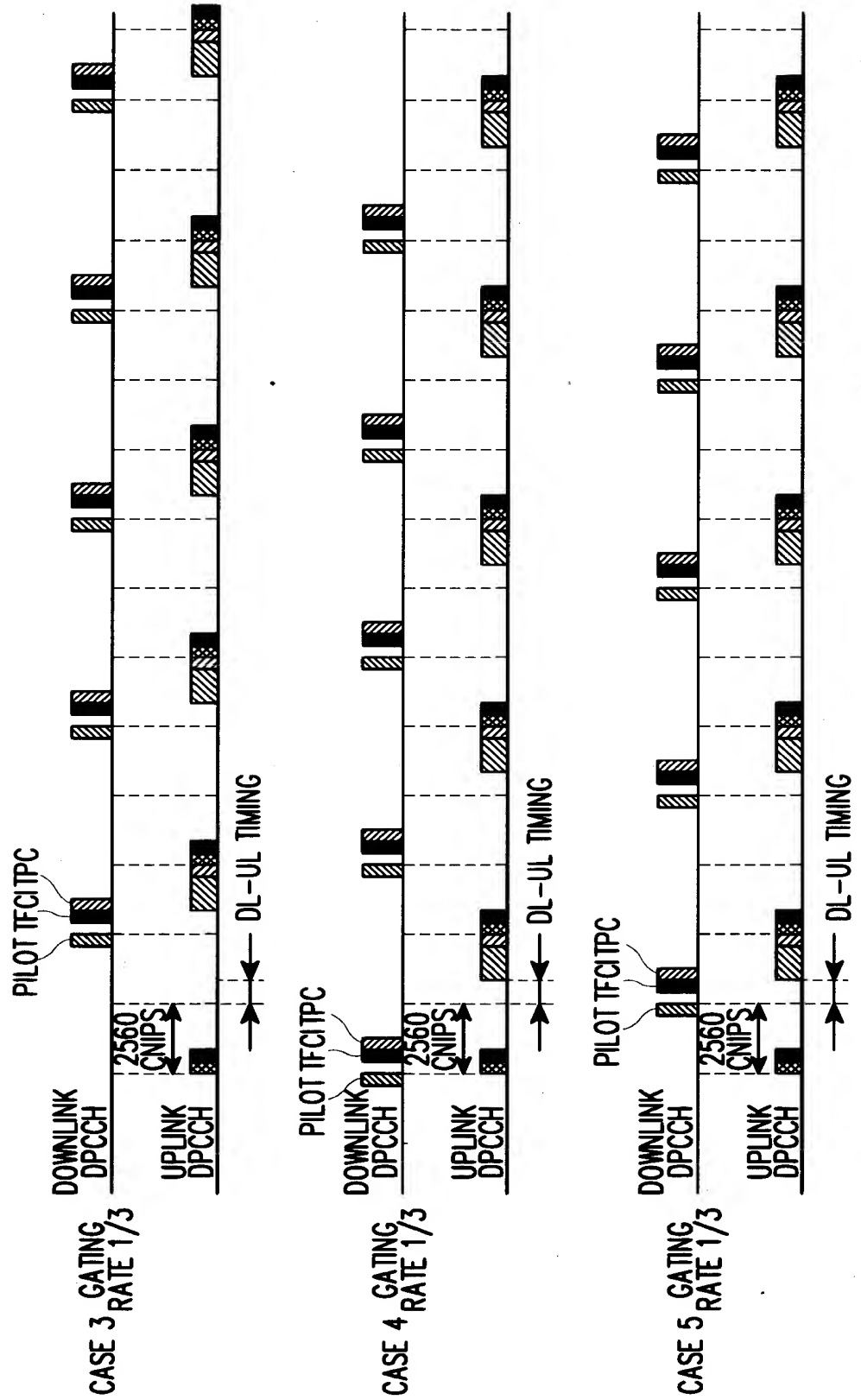


FIG. 12C

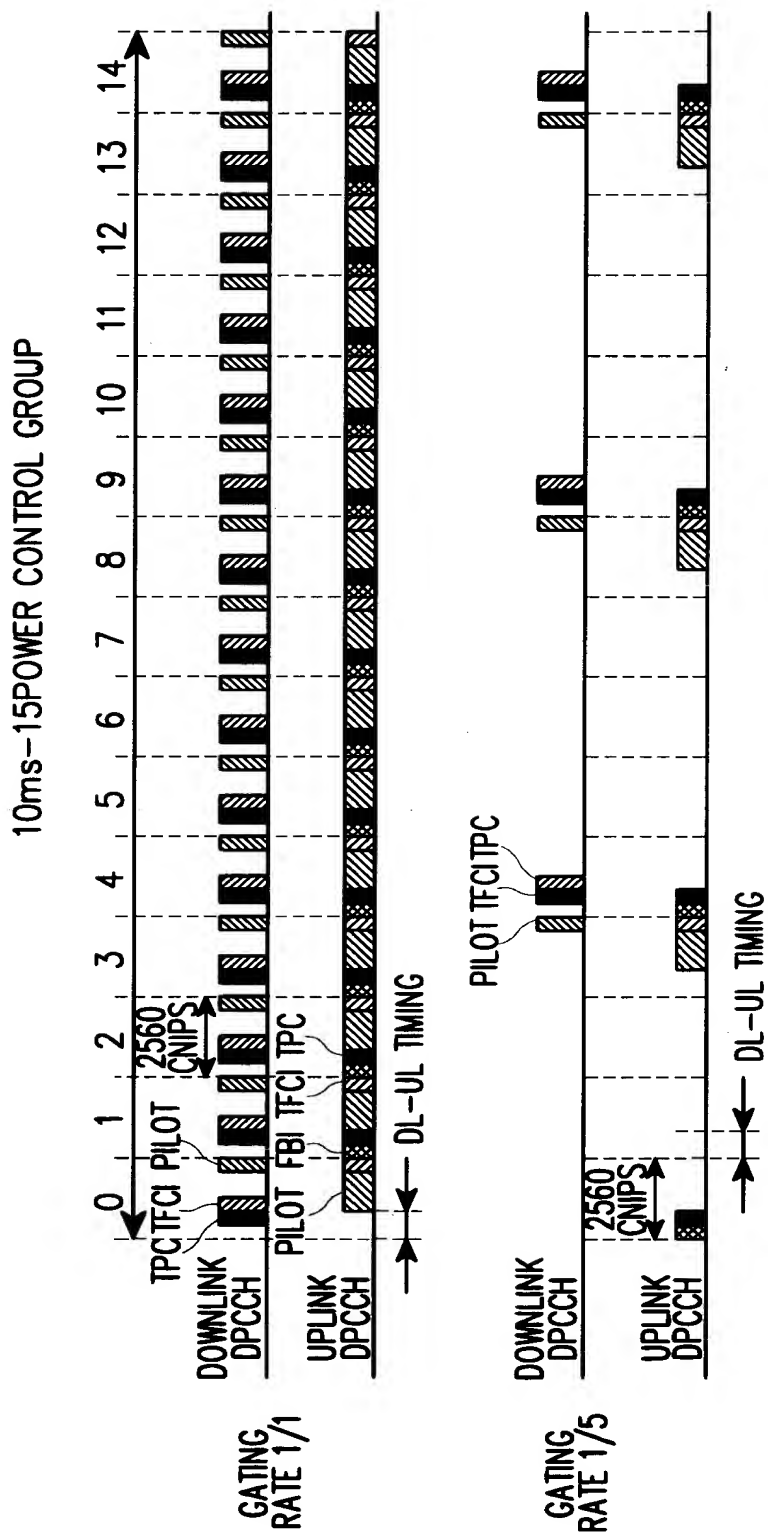


FIG. 12D

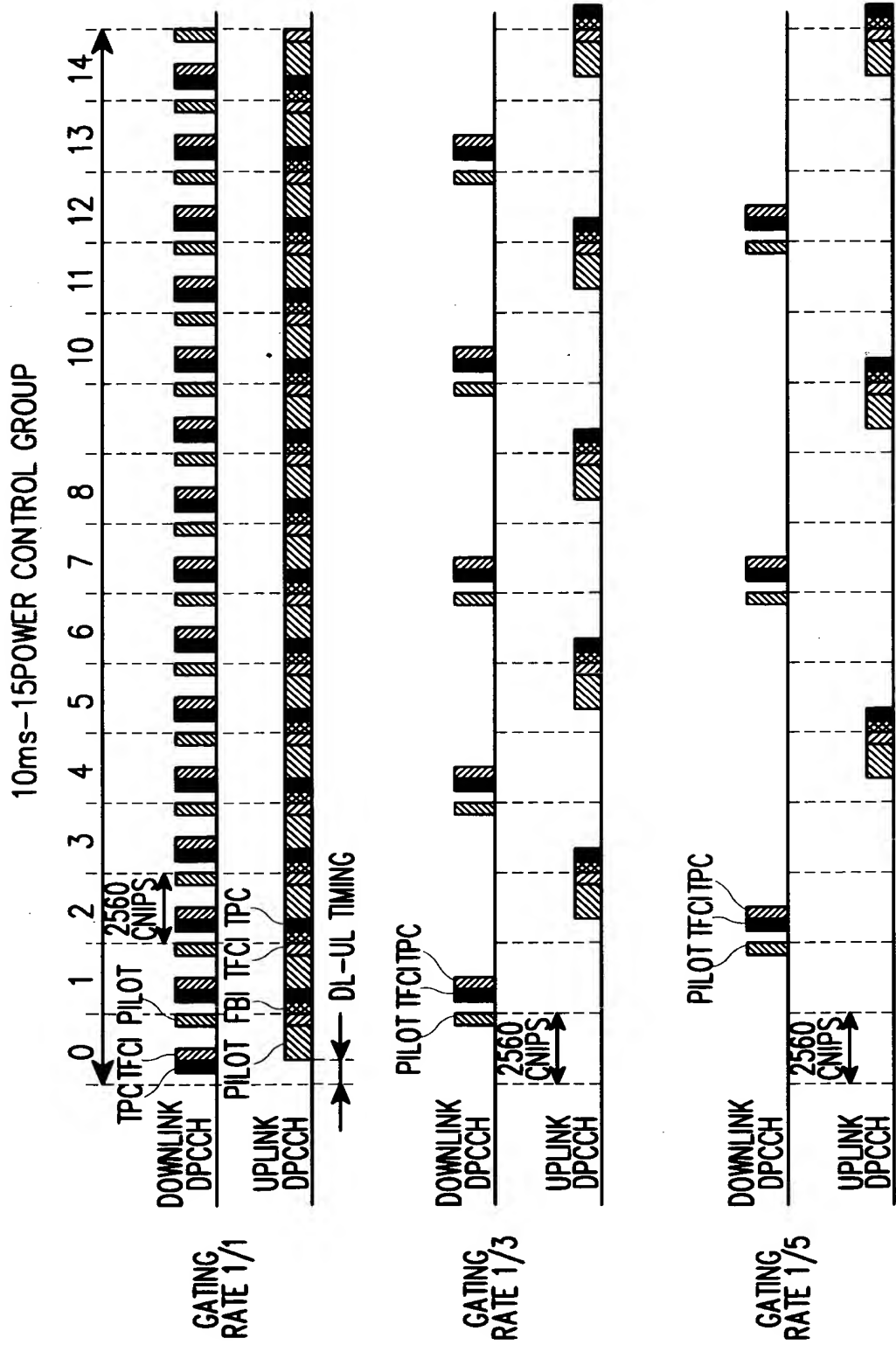


FIG. 12E

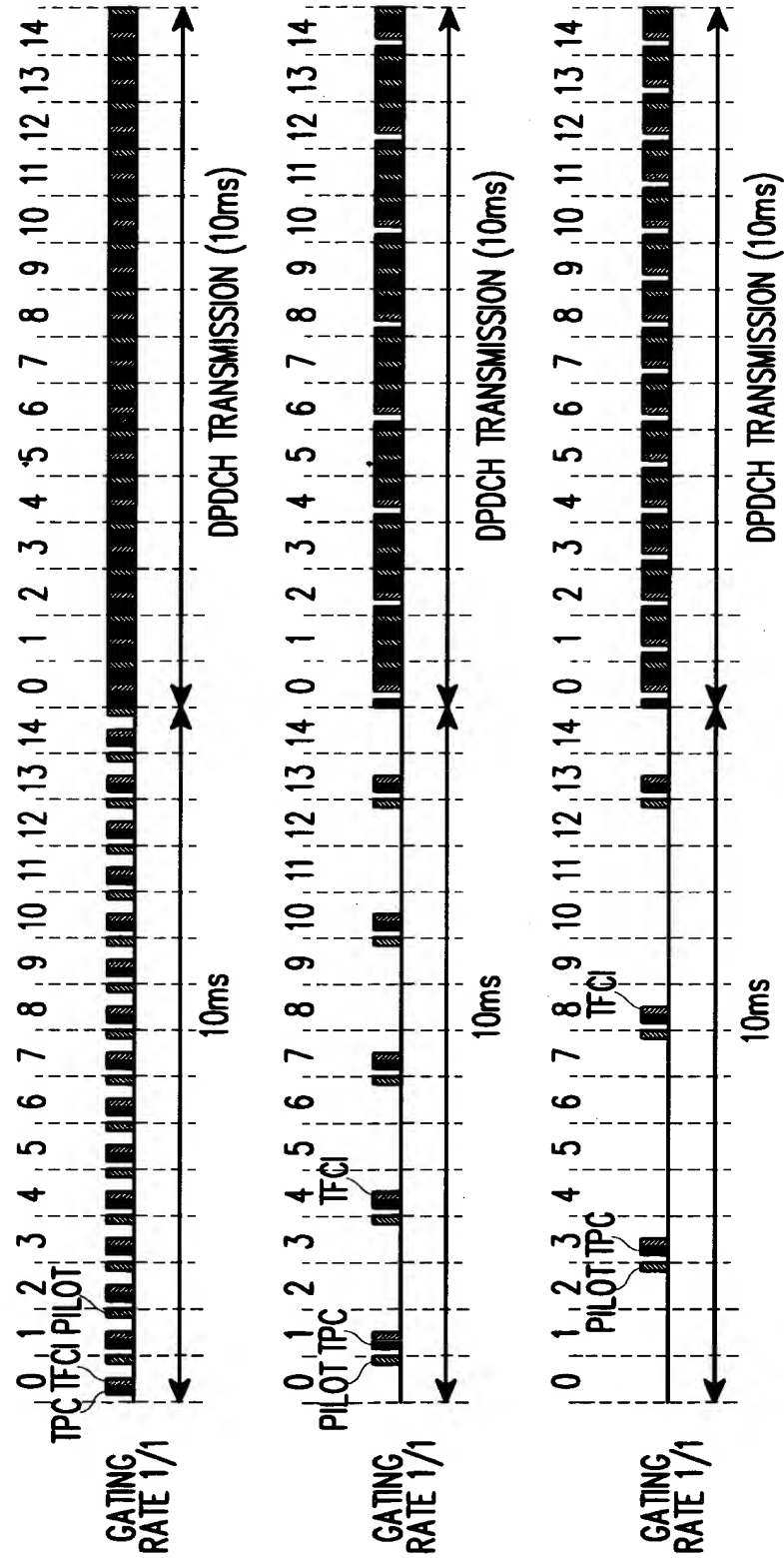


FIG. 13A

$10\text{ms} = 15 \text{ POWER CONTROL GROUP}$
 $x = \text{SFN} * m \quad (0 < m < 36)$

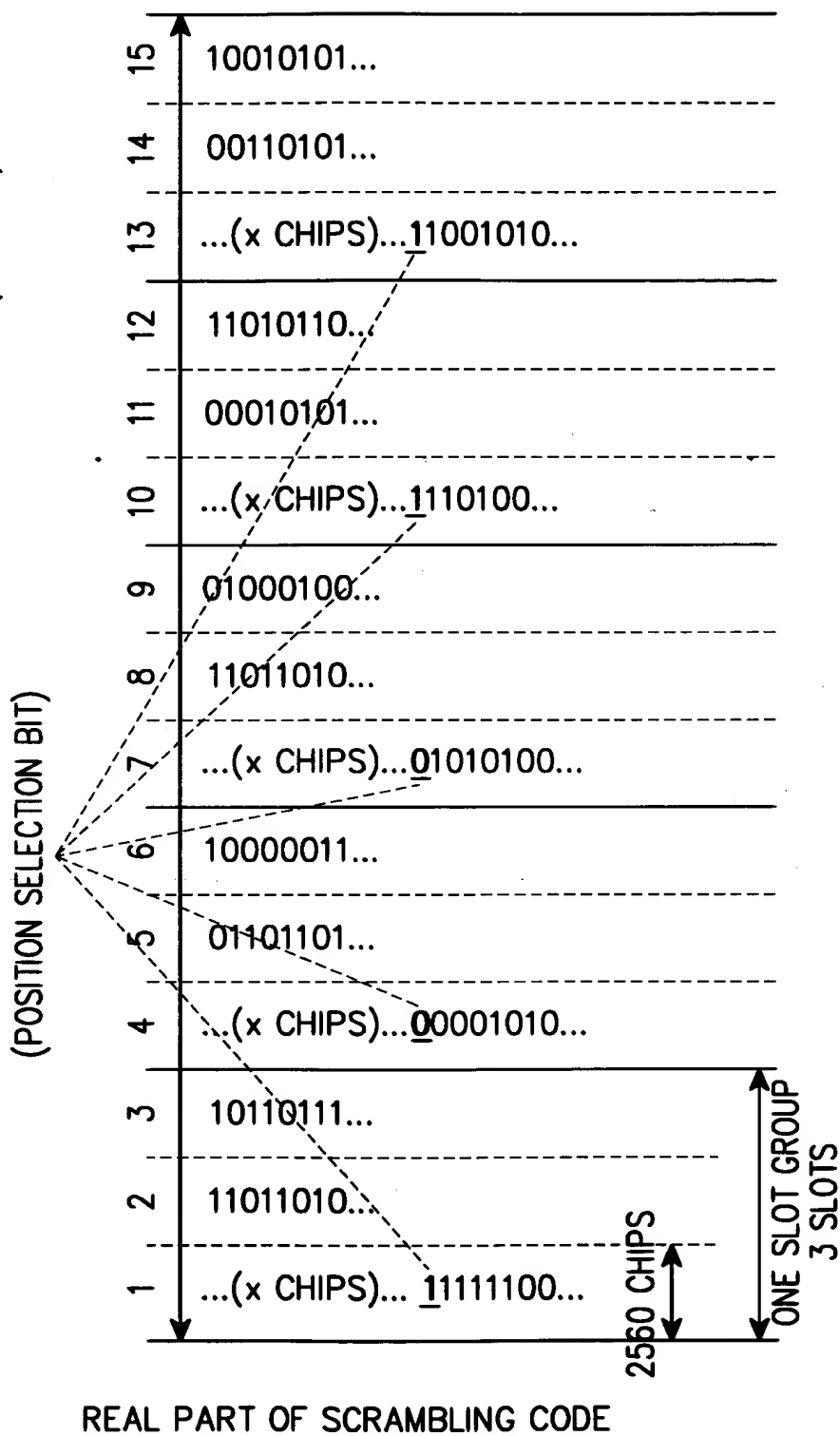
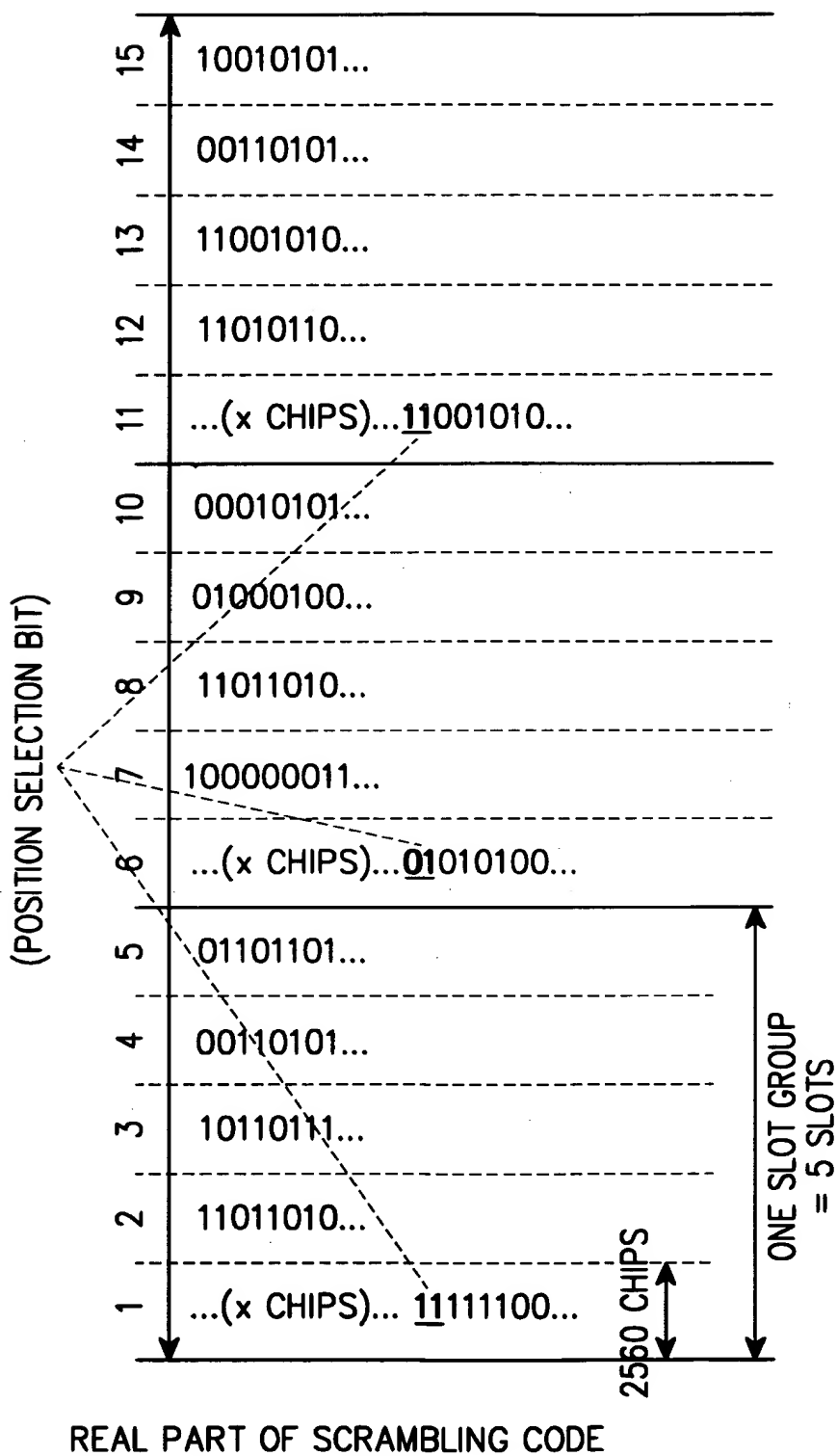


FIG. 13B

$10\text{ms}=15$ POWER CONTROL GROUP
 $x = \text{SFN} * m \ (0 < m < 36)$

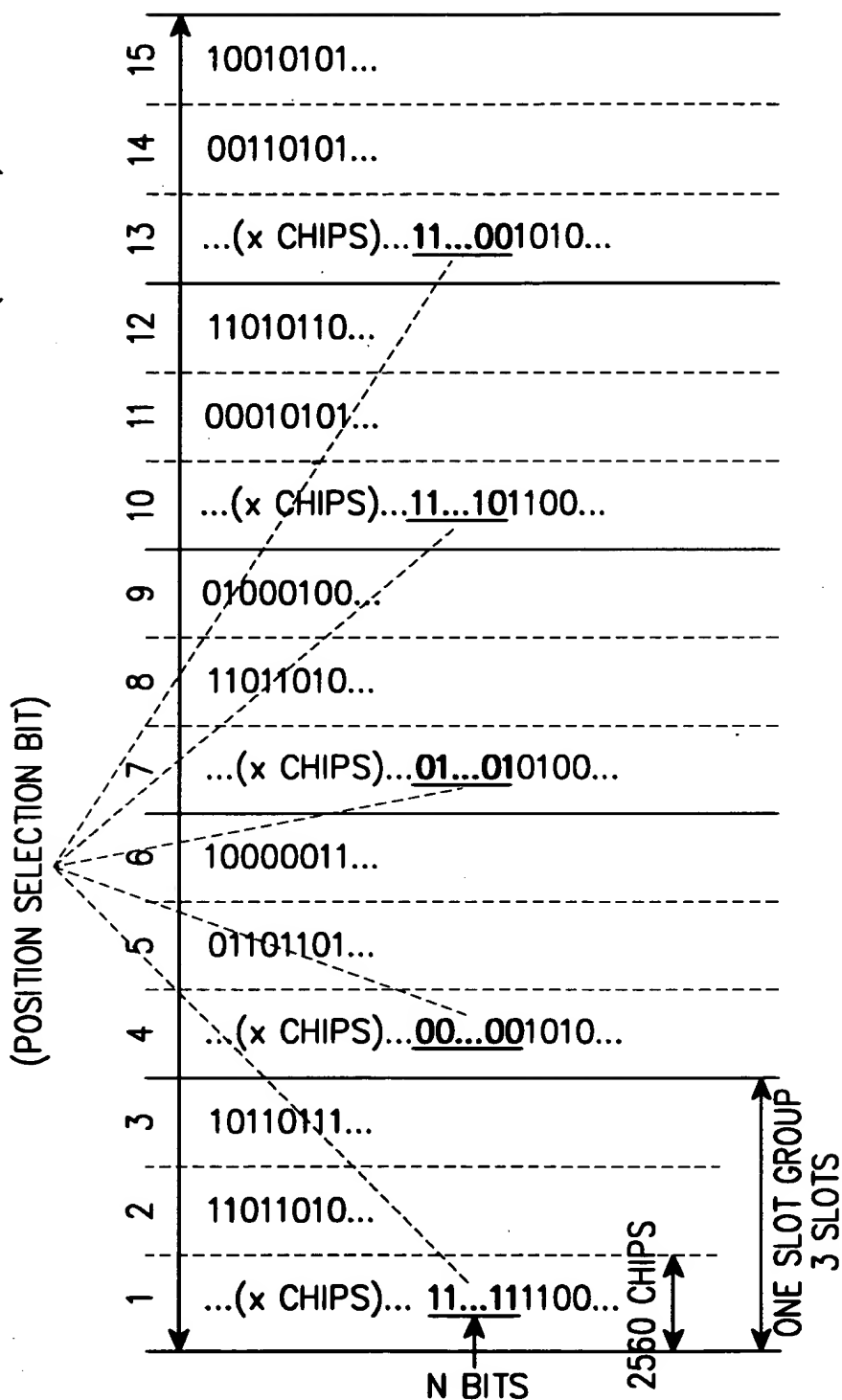


GATING
 RATE
 $1/5$



FIG. 13C

10ms=15 POWER CONTROL GROUP
 $x = \text{SFN} * m \ (0 < m < 36)$



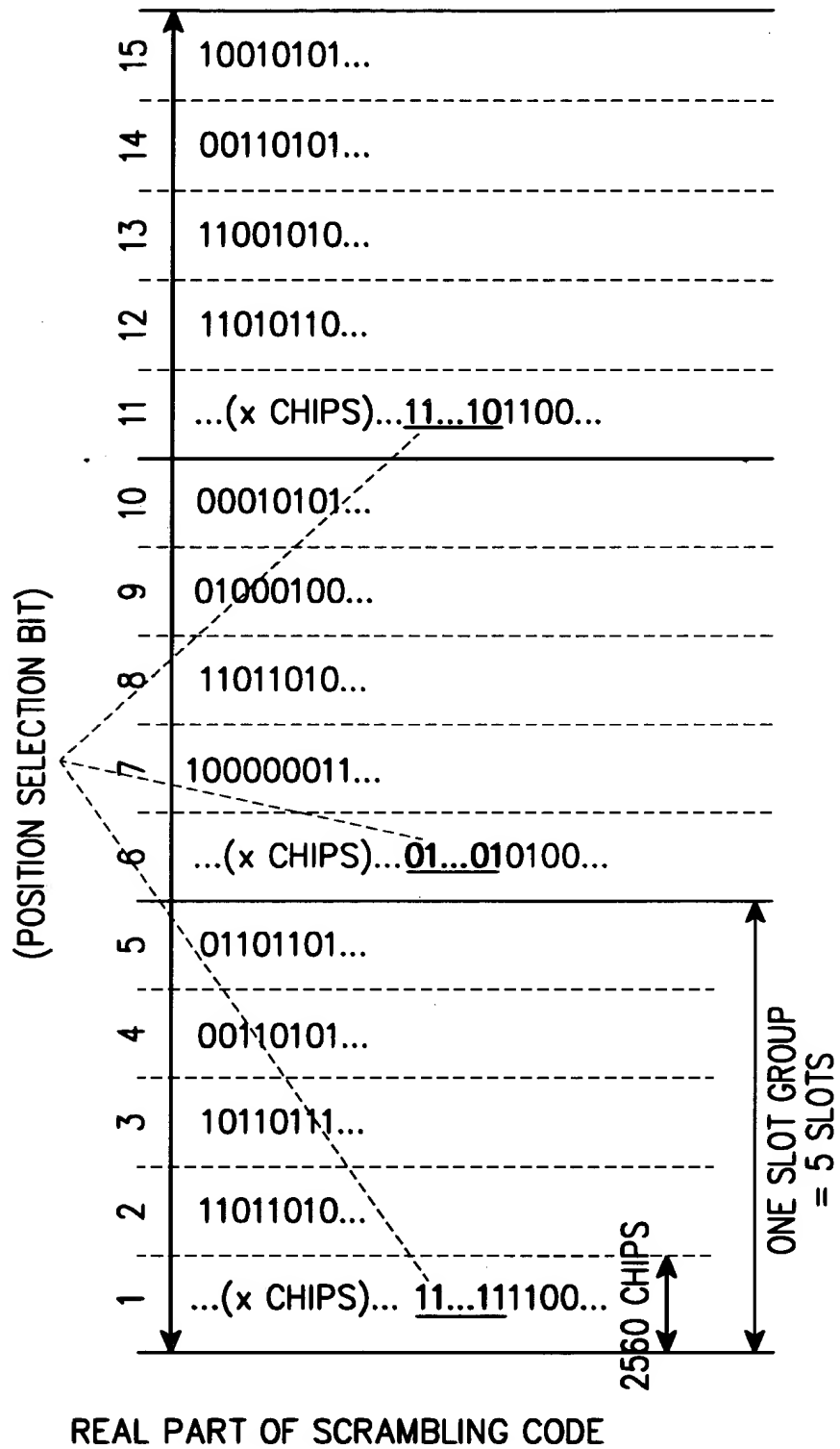
REAL PART OF SCRAMBLING CODE

GATING
RATE
1/3



FIG. 13D

10ms=15 POWER CONTROL GROUP
 $x = \text{SFN} * m \ (0 < m < 36)$



GATING
 RATE
 $1/5$

FIG. 14A

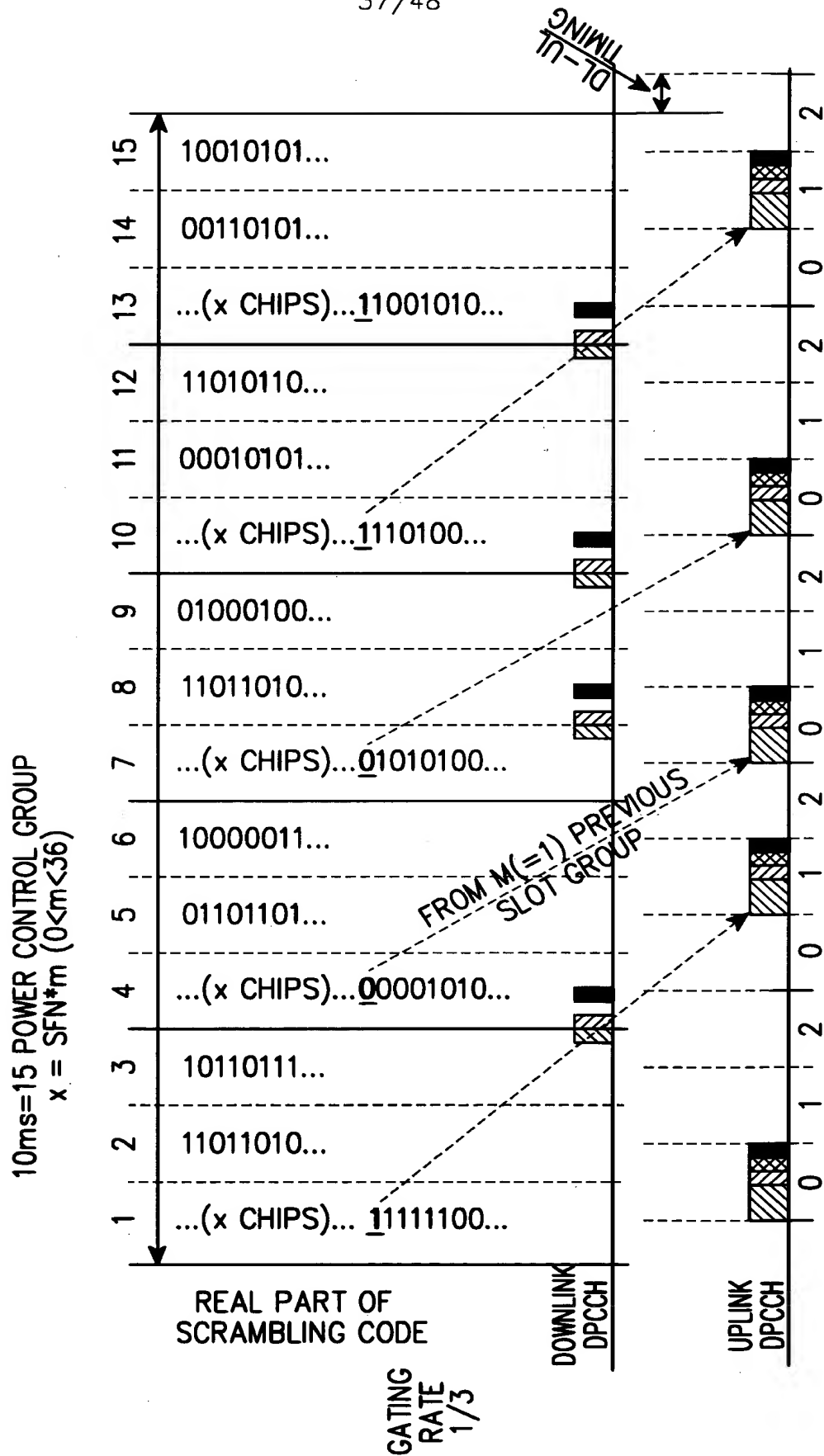


FIG. 14B

$10\text{ms}=15$ POWER CONTROL GROUP
 $x = \text{SFN} * m \ (0 \leq m < 36)$

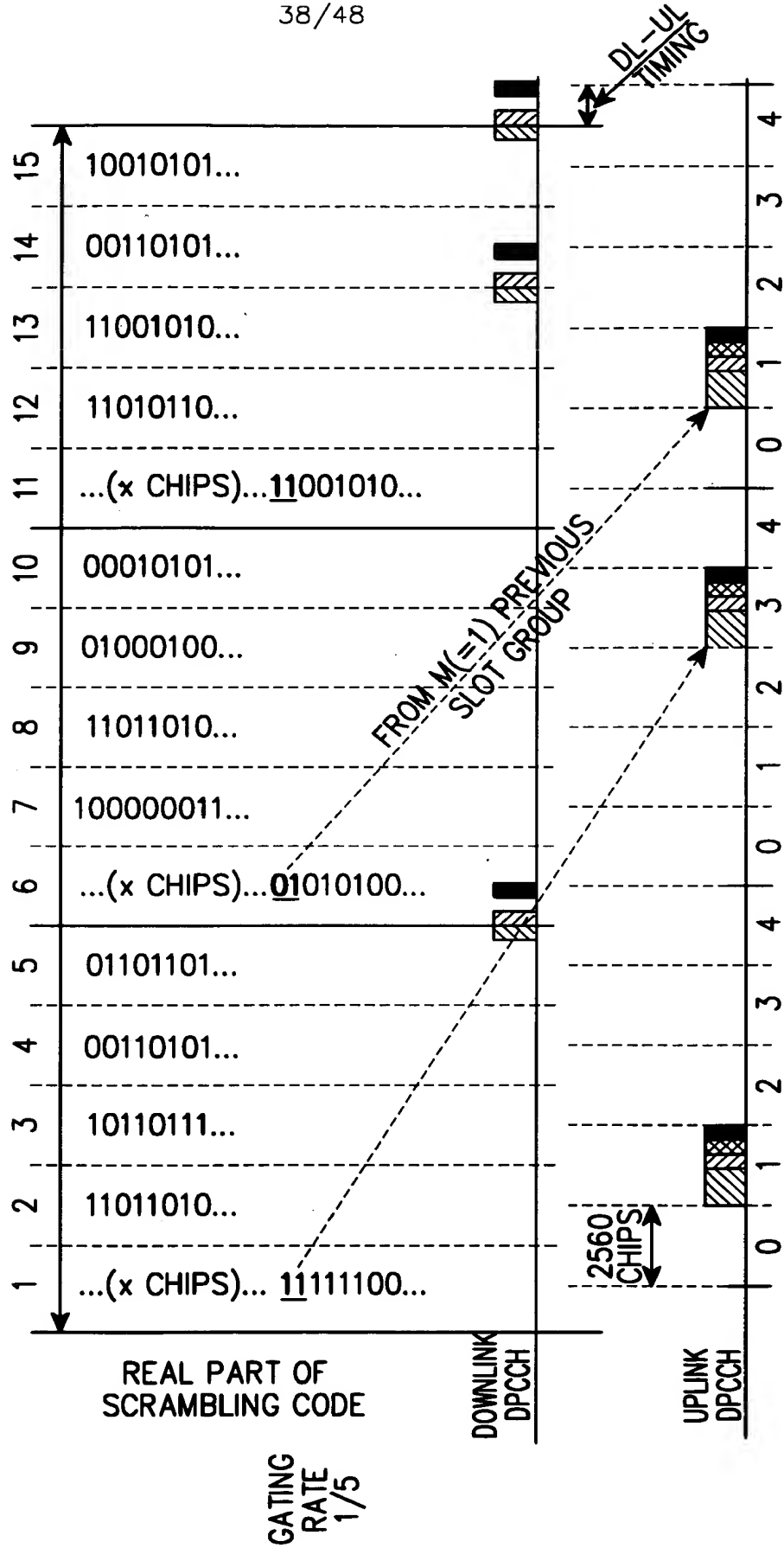


FIG. 14C

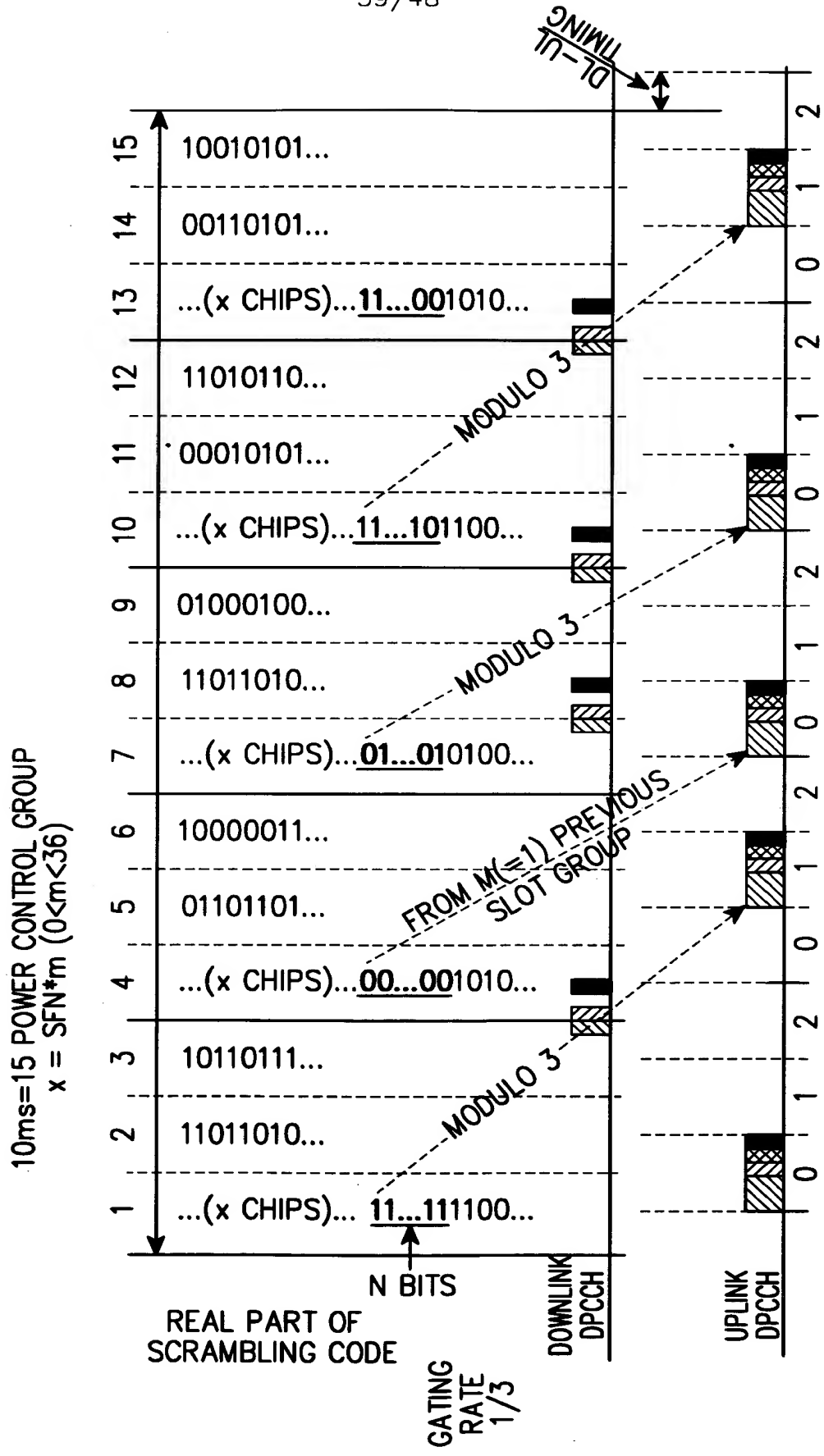


FIG. 14D

$10\text{ms}=15$ POWER CONTROL GROUP
 $x = \text{SFN} \cdot m \ (0 \leq m < 36)$

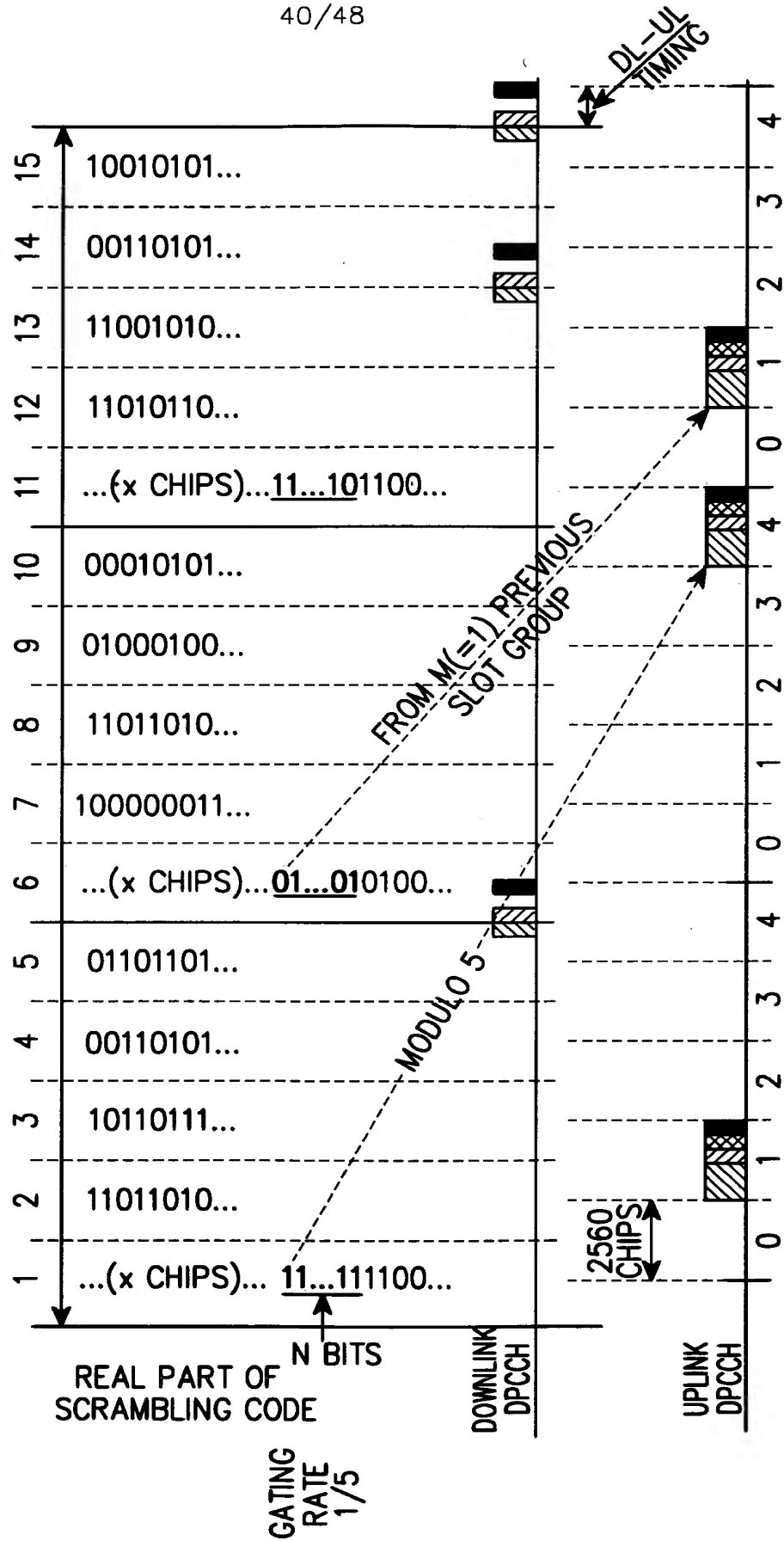




FIG. 15A

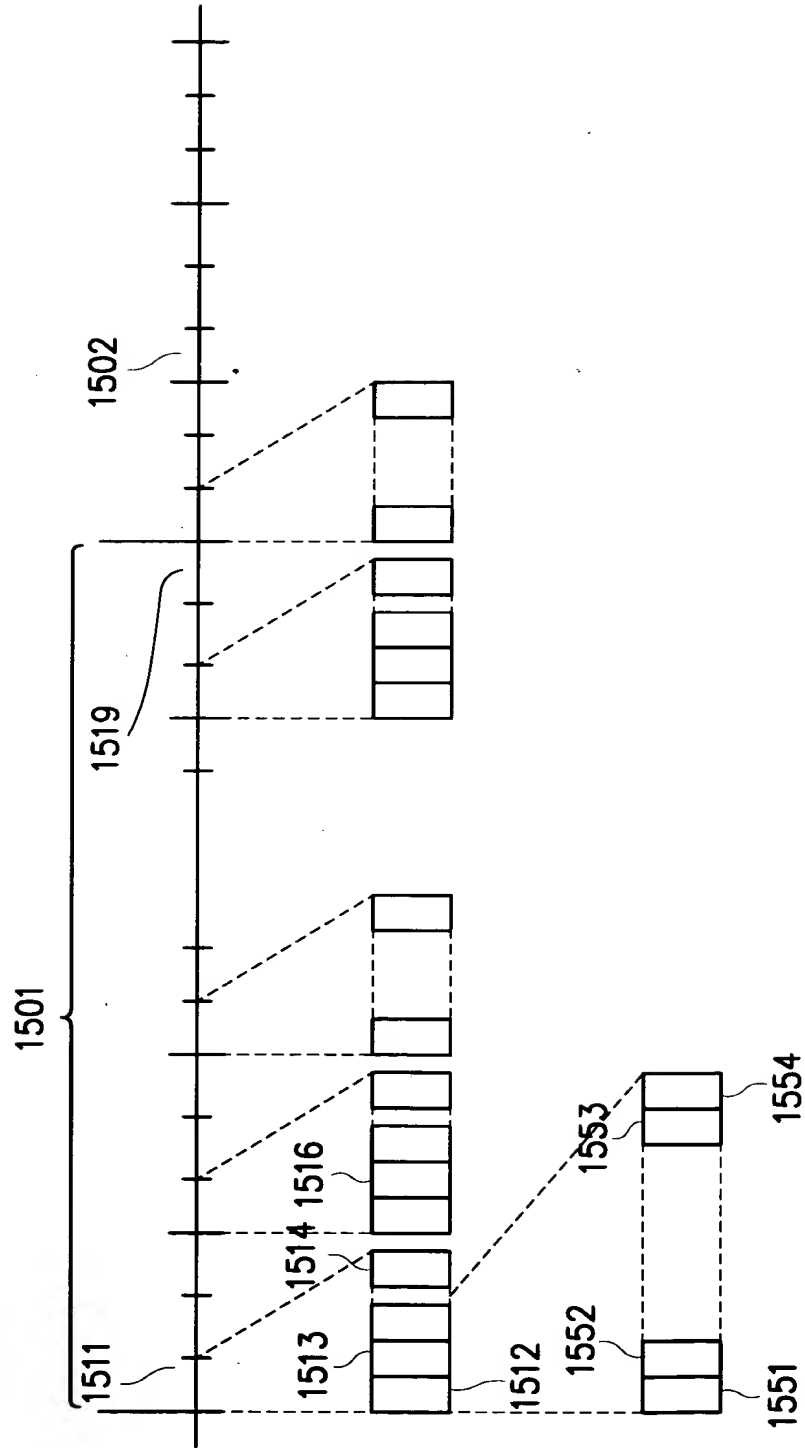


FIG. 15B

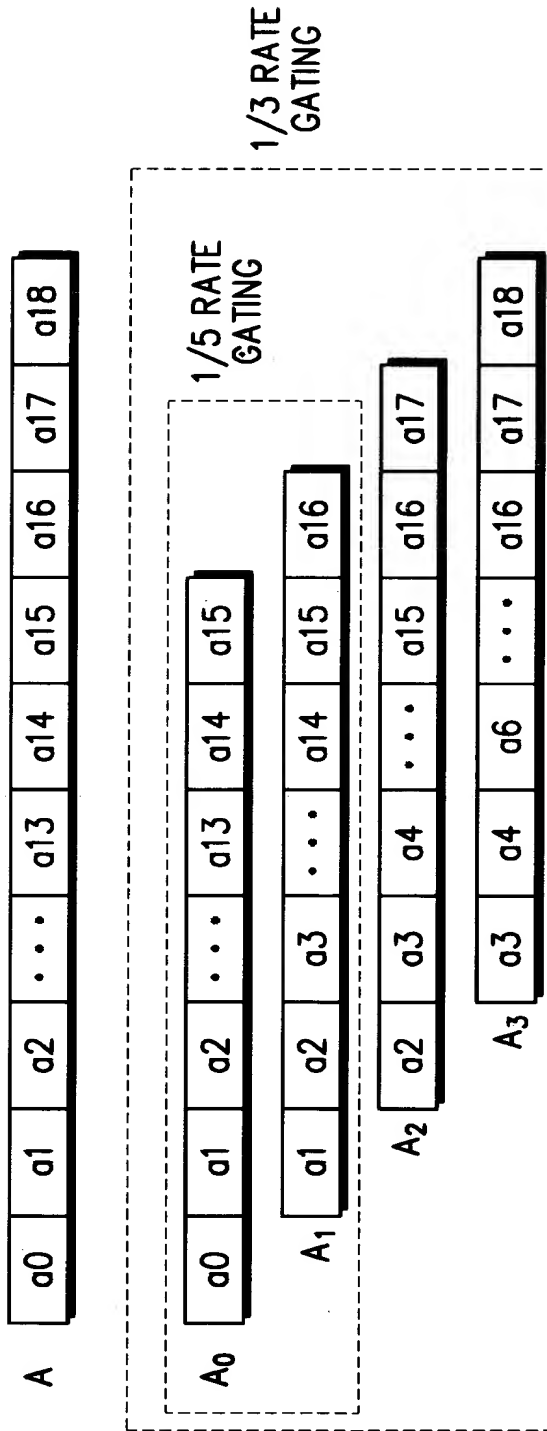


FIG. 15C

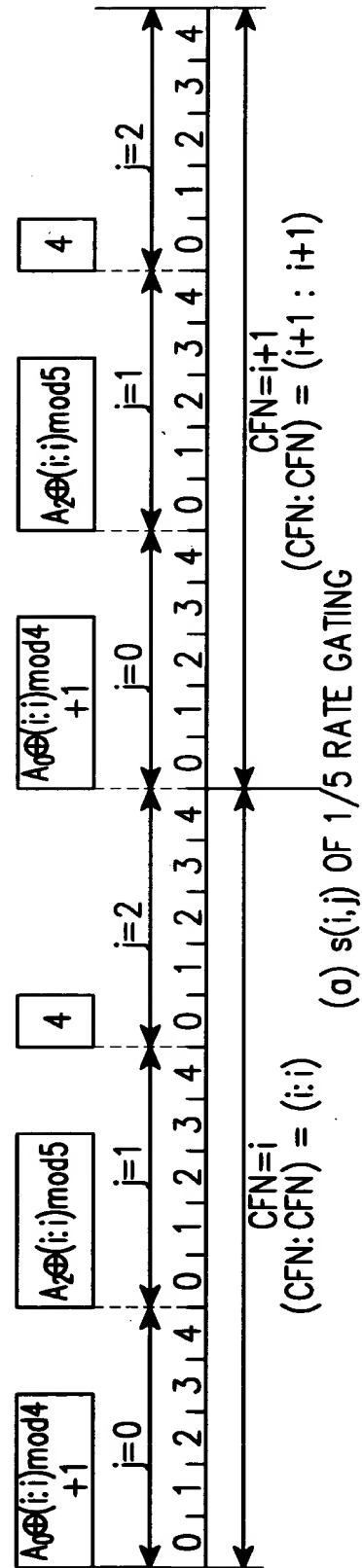
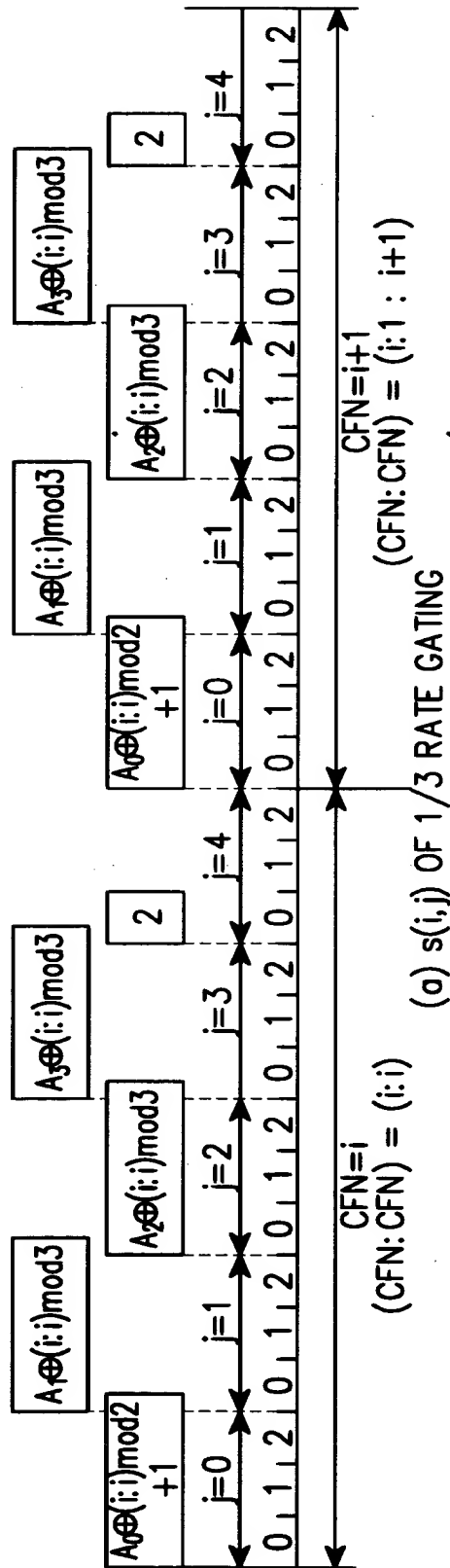


FIG. 16

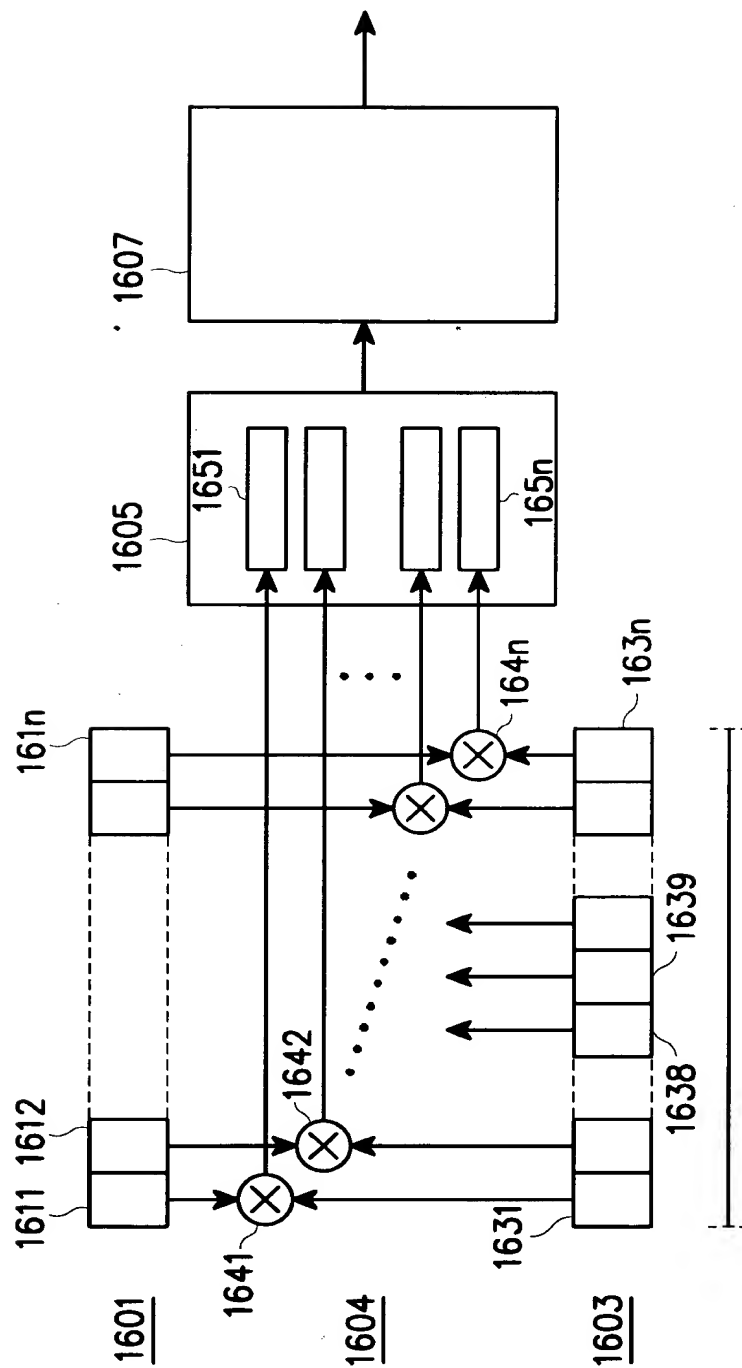


FIG. 17A

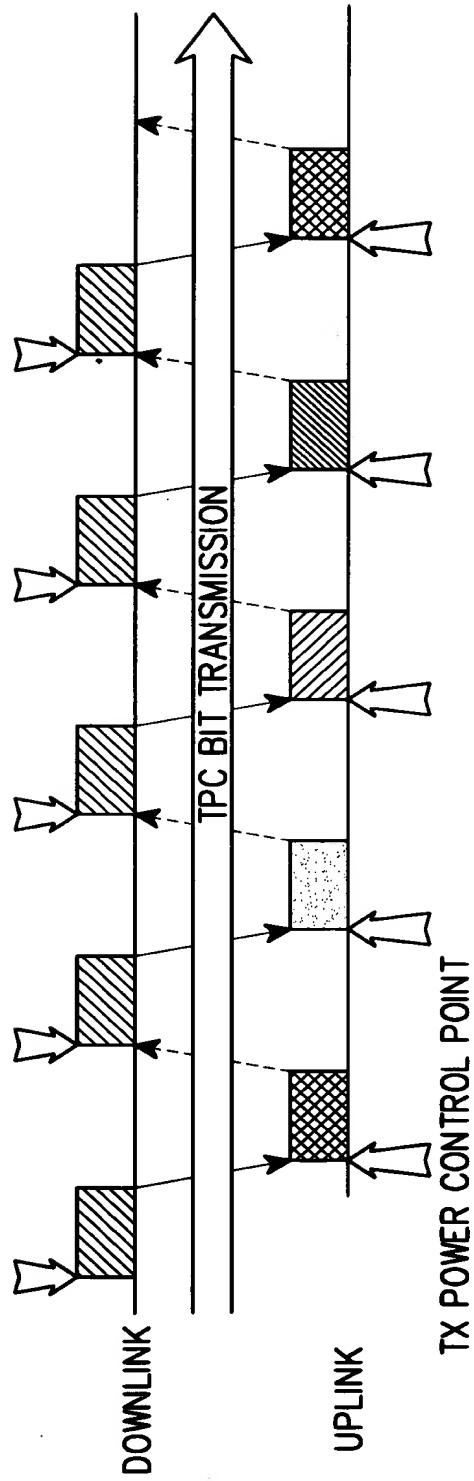


FIG. 17B

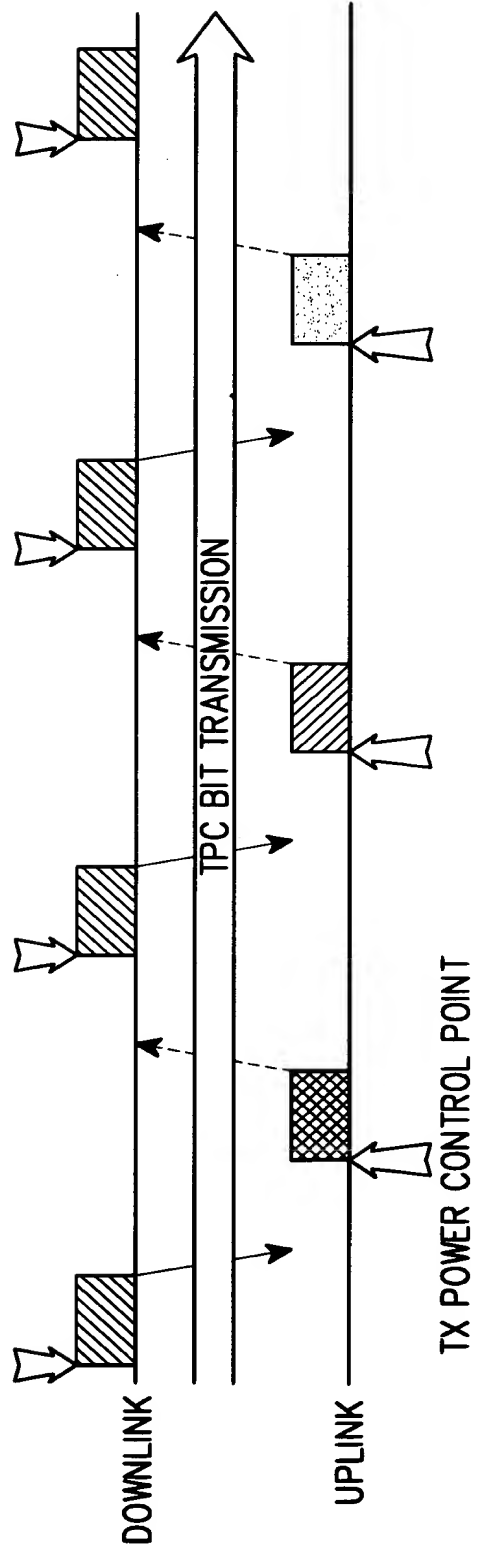


FIG. 18A

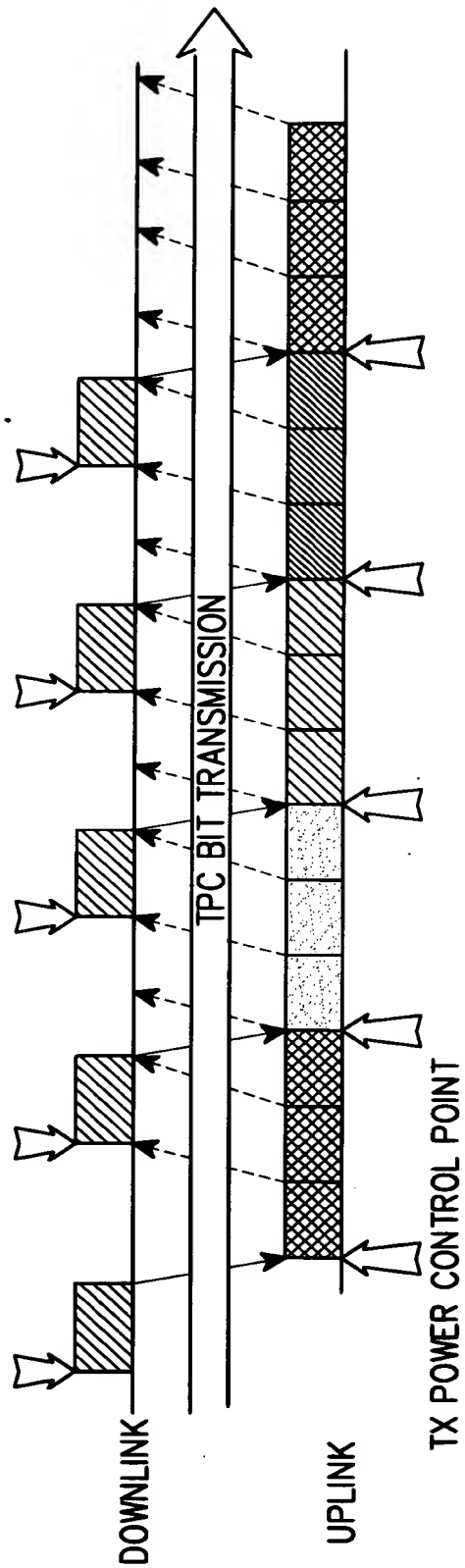


FIG. 18B

